



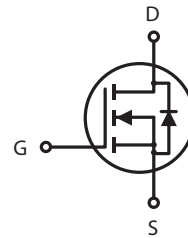
## N-Channel Enhancement Mode Field Effect Transistor

### PRODUCT SUMMARY

V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> (mΩ) Typ
100V	15A	110 @ V <sub>GS</sub> =10V
		121 @ V <sub>GS</sub> =4.5V

### FEATURES

- Super high dense cell design for low R<sub>DS(ON)</sub>.
- Rugged and reliable.
- TO-220 and TO-220F Package.



### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub>=25°C unless otherwise noted)

Symbol	Parameter	TO-220	TO-220F	Units
V <sub>DS</sub>	Drain-Source Voltage	100		V
V <sub>GS</sub>	Gate-Source Voltage	±20	±20	V
I <sub>D</sub>	Drain Current-Continuous <sup>a</sup>	15	15 <sup>e</sup>	A
		12.6	12.6 <sup>e</sup>	A
I <sub>DM</sub>	-Pulsed <sup>b</sup>	45	45 <sup>e</sup>	A
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>d</sup>	25		mJ
P <sub>D</sub>	Maximum Power Dissipation <sup>a</sup>	58	25	W
		40	17.5	W
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to 175		°C

### THERMAL CHARACTERISTICS

R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case <sup>a</sup>	2.6	6	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient <sup>a</sup>	62.5	62.5	°C/W

# STP15L01/F

Ver1.0

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	100			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =80V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> = ±20V , V <sub>DS</sub> =0V			±100	nA
ON CHARACTERISTICS						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	1.7	3	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =7A		110	138	m-ohm
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =6A		121	163	m-ohm
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =20V , I <sub>D</sub> =7A		15		S
DYNAMIC CHARACTERISTICS °						
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V f=1.0MHz		675		pF
C <sub>OSS</sub>	Output Capacitance			48		pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			32		pF
SWITCHING CHARACTERISTICS °						
t <sub>D(ON)</sub>	Turn-On Delay Time	V <sub>DD</sub> =50V I <sub>D</sub> =1A V <sub>GS</sub> =10V R <sub>GEN</sub> = 6 ohm		16		ns
t <sub>r</sub>	Rise Time			14.5		ns
t <sub>D(OFF)</sub>	Turn-Off Delay Time			31		ns
t <sub>f</sub>	Fall Time			6.4		ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =50V,I <sub>D</sub> =7A,V <sub>GS</sub> =10V		12.4		nC
		V <sub>DS</sub> =50V,I <sub>D</sub> =7A,V <sub>GS</sub> =4.5V		6.4		nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =50V,I <sub>D</sub> =7A, V <sub>GS</sub> =10V		1.7		nC
Q <sub>gd</sub>	Gate-Drain Charge			3.5		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V,I <sub>S</sub> =2A		0.8	1.3	V

### Notes

- Surface Mounted on FR4 Board, t ≤ 10sec.
- Pulse Test: Pulse Width ≤ 300us, Duty Cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.
- Starting T<sub>J</sub>=25°C, L=0.5mH, V<sub>DD</sub> = 50V. (See Figure12)
- Drain current limited by maximum junction temperature.

Nov,01,2010

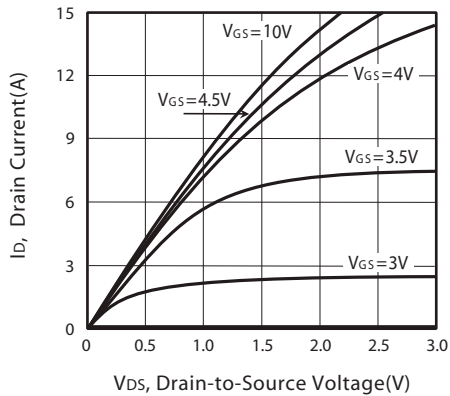


Figure 1. Output Characteristics

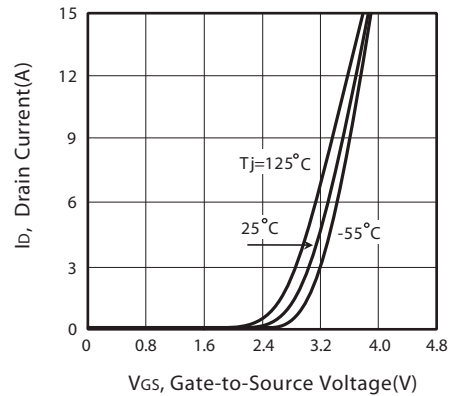


Figure 2. Transfer Characteristics

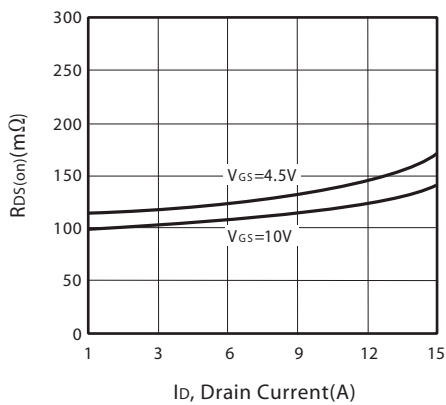


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

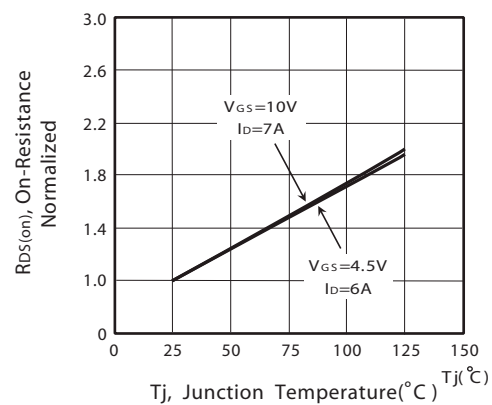


Figure 4. On-Resistance Variation with Drain Current and Temperature

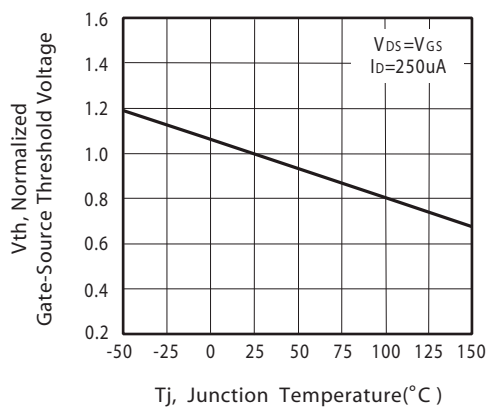


Figure 5. Gate Threshold Variation with Temperature

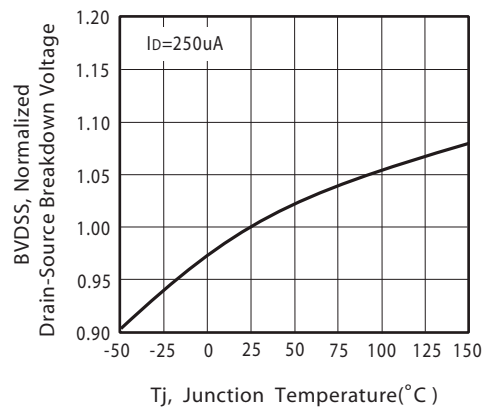


Figure 6. Breakdown Voltage Variation with Temperature

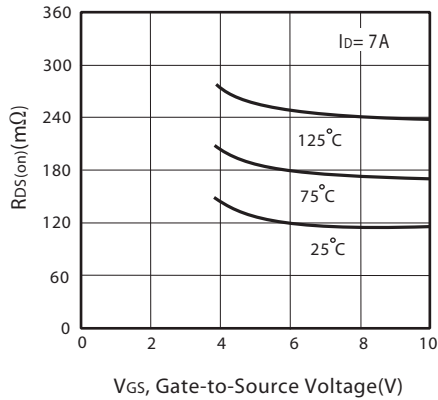


Figure 7. On-Resistance vs. Gate-Source Voltage

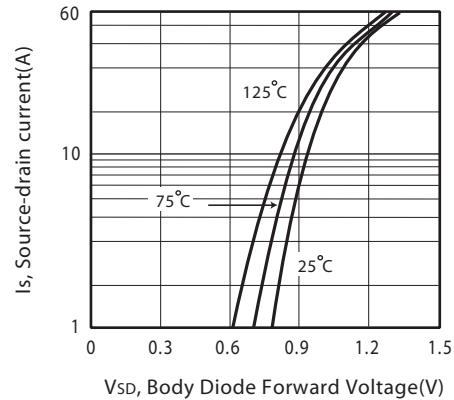


Figure 8. Body Diode Forward Voltage Variation with Source Current

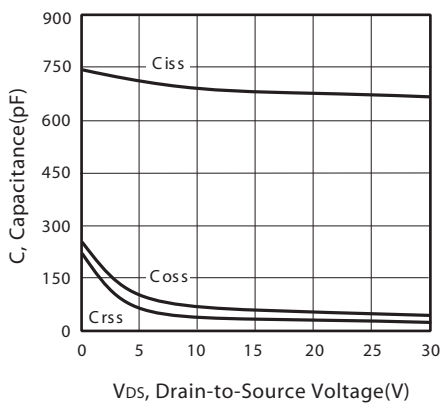


Figure 9. Capacitance

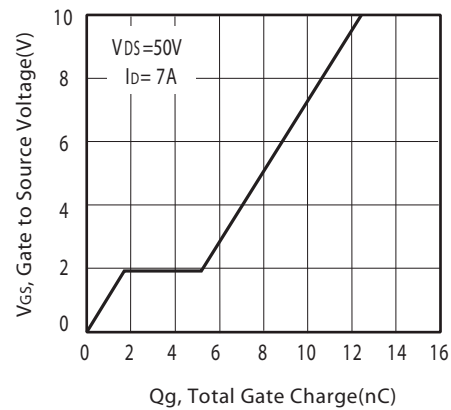


Figure 10. Gate Charge

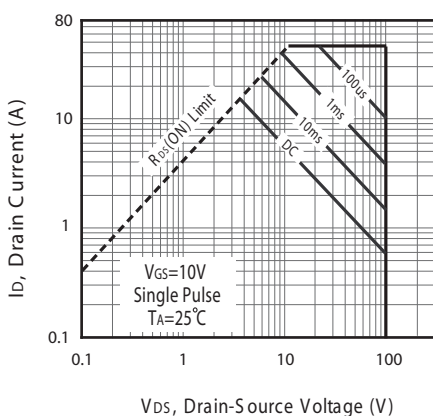


Figure 11a. Maximum Safe Operating Area for STP15L01

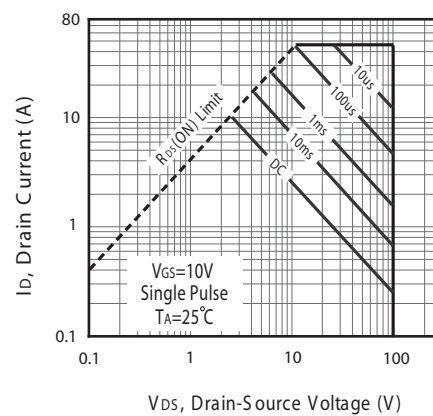
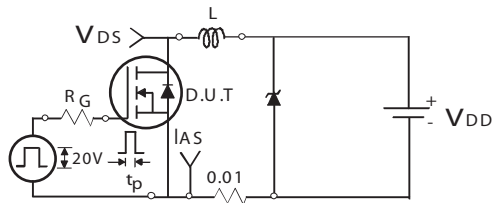


Figure 11b. Maximum Safe Operating Area for STP15L01F

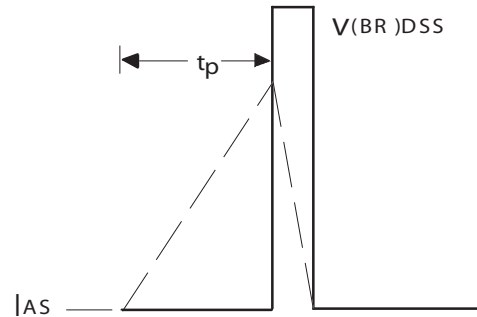
# STP15L01/F

Ver 1.0



Unclamped Inductive Test Circuit

Figure 12a.



Unclamped Inductive Waveforms

Figure 12b.

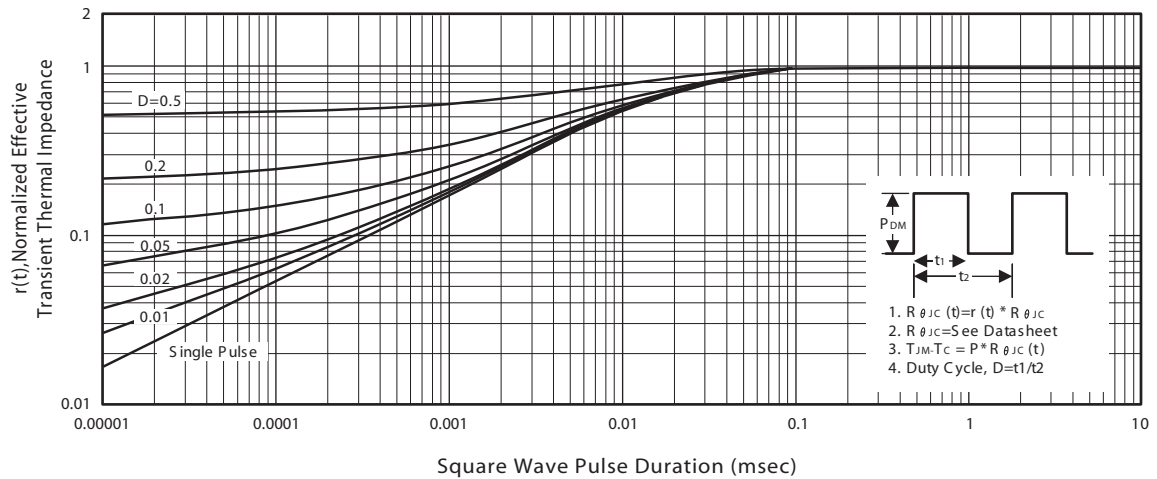


Figure 13a. Normalized Thermal Transient Impedance Curve for STP15L01

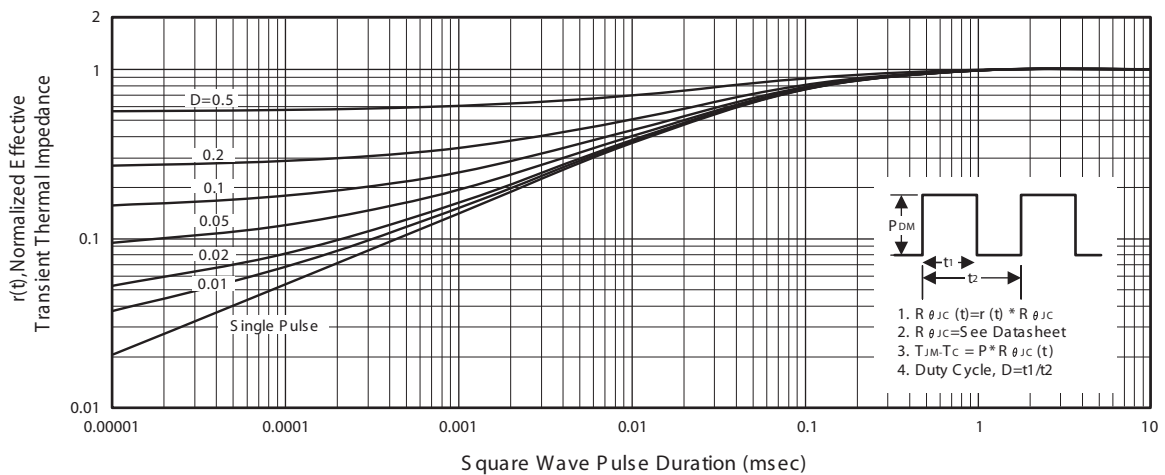


Figure 13b. Normalized Thermal Transient Impedance Curve for STP15L01F

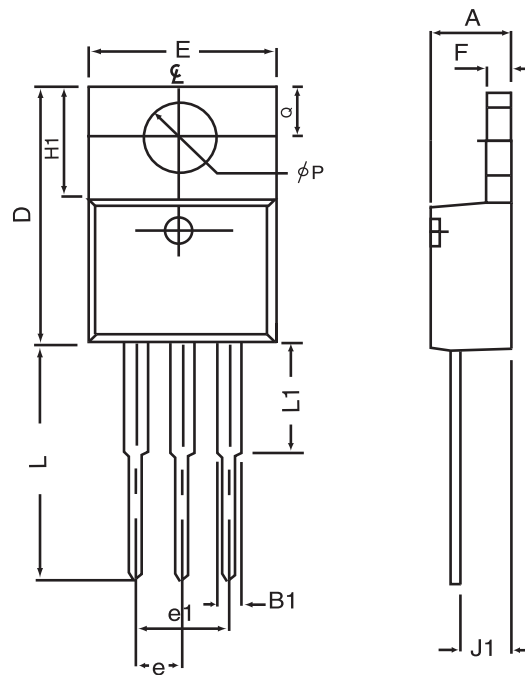
Nov,01,2010

# STP15L01/F

Ver 1.0

## PACKAGE OUTLINE DIMENSIONS

### TO-220



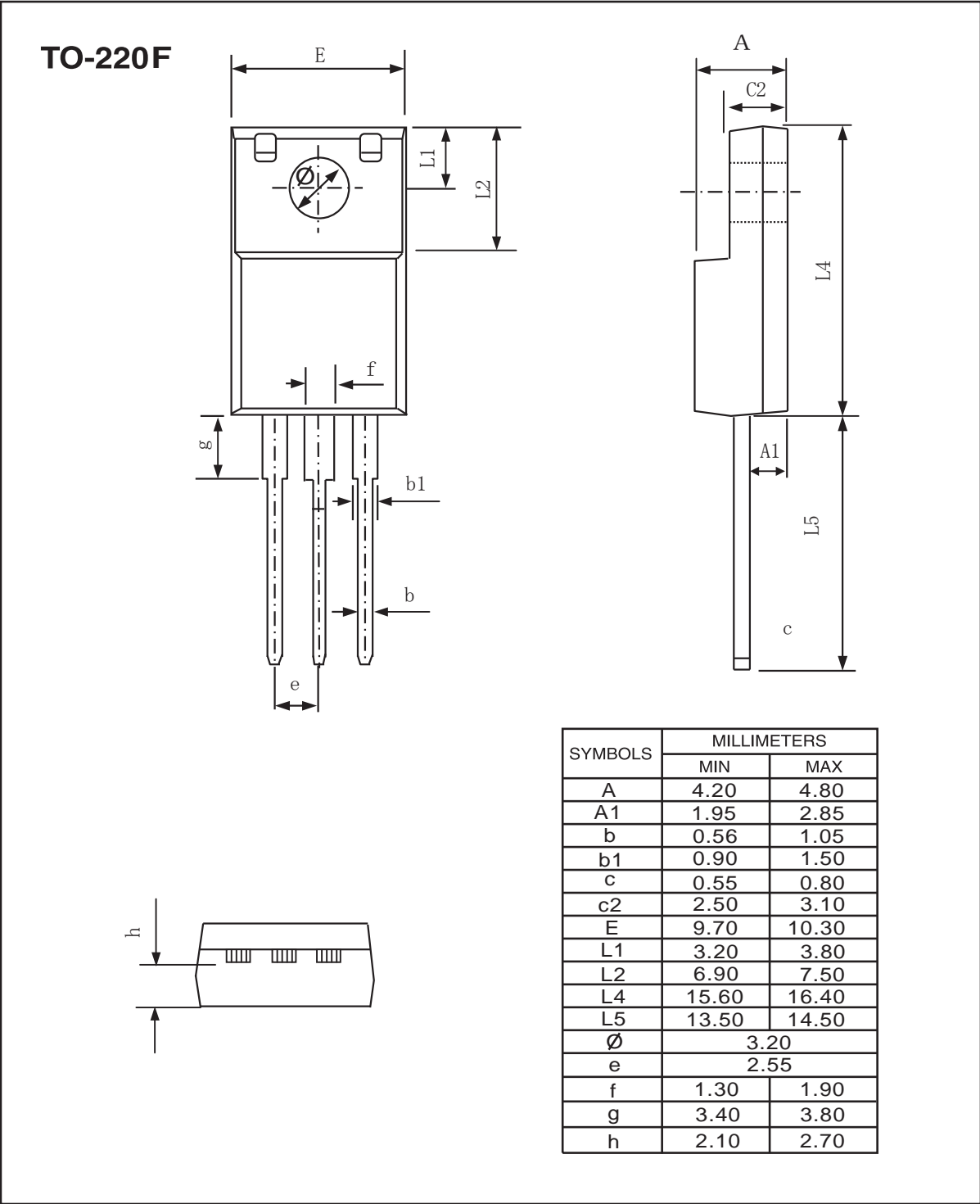
SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.32	4.80	0.170	0.189
B1	1.27	1.65	0.050	0.630
D	14.6	16.00	0.575	0.610
E	9.70	10.41	0.382	0.410
e	2.34	2.74	0.092	0.108
e1	4.68	5.48	0.184	0.216
F	1.14	1.40	0.045	0.055
H1	5.97	6.73	0.235	0.265
J1	2.20	2.79	0.087	0.110
L	12.88	14.22	0.507	0.560
L1	3.00	6.35	0.120	0.250
φP	3.50	3.94	0.138	0.155
Q	2.54	3.05	0.100	0.120

Nov,01,2010

# STP15L01/F

Ver 1.0

## PACKAGE OUTLINE DIMENSIONS

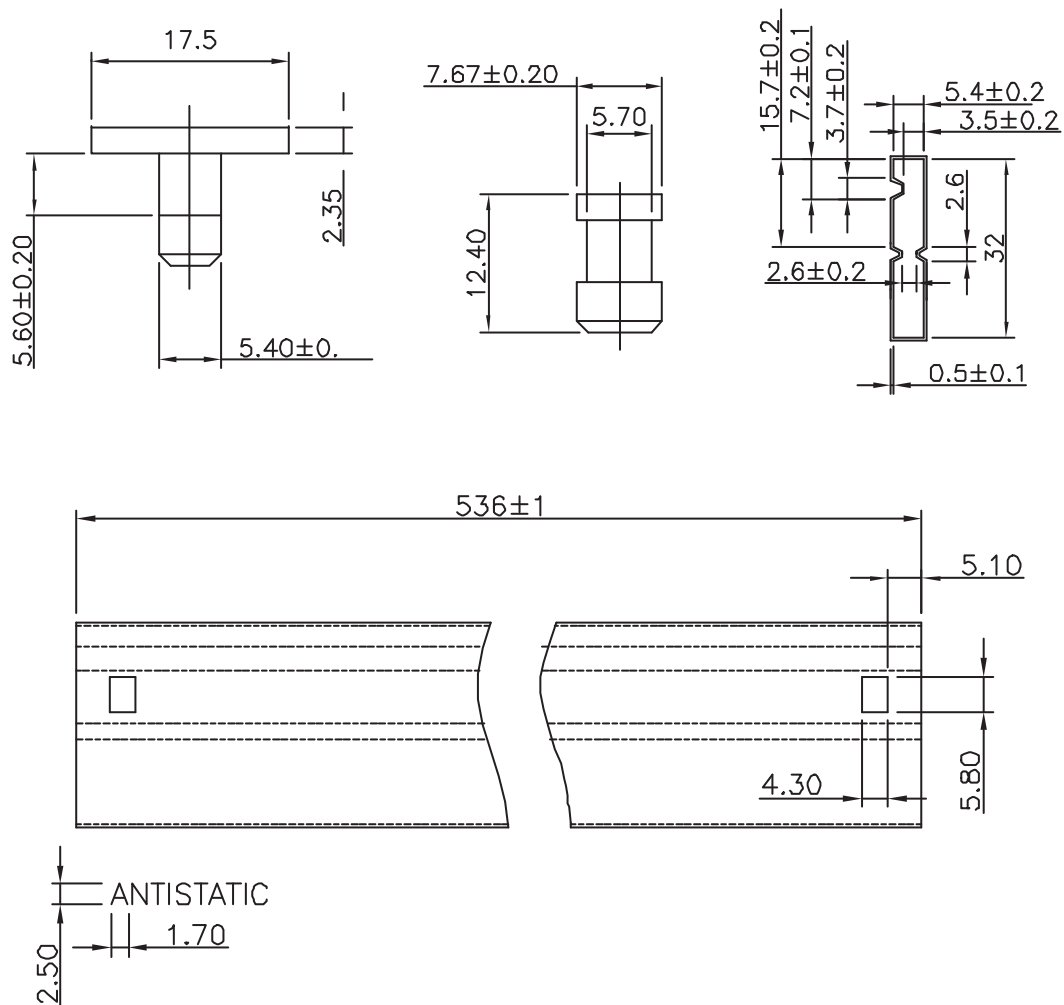


Nov,01,2010

# STP15L01/F

Ver 1.0

## TO-220 Tube



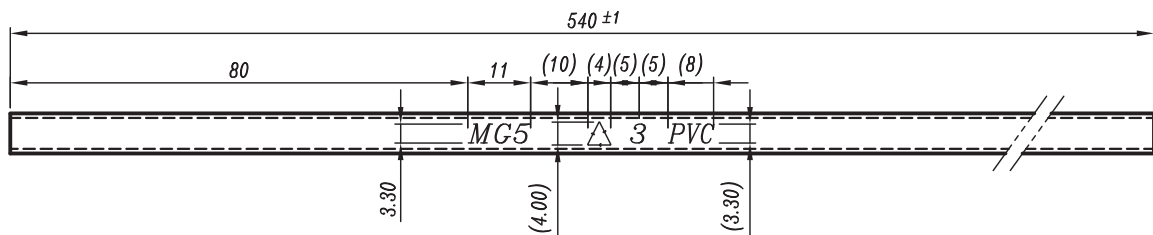
Nov,01,2010



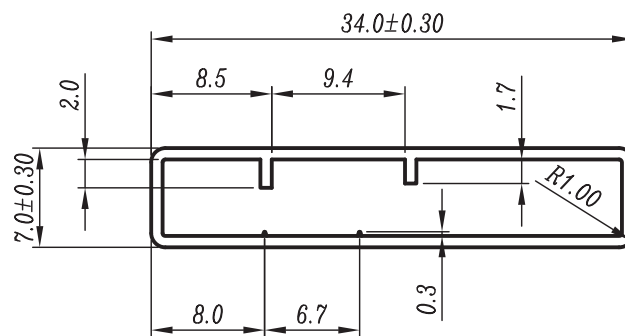
# STP15L01/F

Ver 1.0

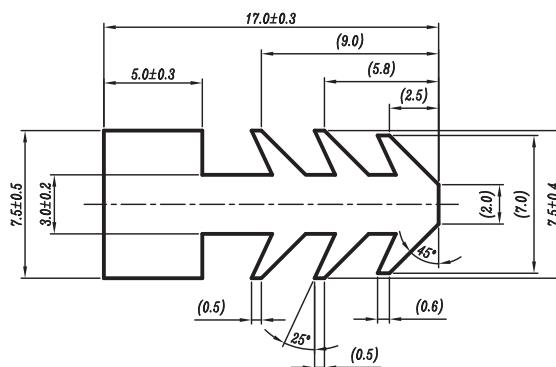
## TO-220F Tube



$$t = 0.8 \pm 0.15$$



$$SCALE = 2/1$$



$$L = 8.0^{+0.5}_{-1}$$

Nov,01,2010