



## P-Channel Enhancement Mode Field Effect Transistor

### PRODUCT SUMMARY

V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> (mΩ) Max
-30V	-3.6A	52 @ V <sub>GS</sub> =-10V
		65 @ V <sub>GS</sub> =-4.5V

### FEATURES

- Super high dense cell design for low R<sub>DS(ON)</sub>.
- Rugged and reliable.
- Surface Mount Package.
- ESD Protected.



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Limit	Units	
V <sub>DS</sub>	Drain-Source Voltage	-30	V	
V <sub>GS</sub>	Gate-Source Voltage	±20	V	
I <sub>D</sub>	Drain Current-Continuous <sup>a c</sup>	T <sub>A</sub> =25°C	-3.6	A
		T <sub>A</sub> =70°C	-2.9	A
I <sub>DM</sub>	-Pulsed <sup>c</sup>	-21	A	
P <sub>D</sub>	Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	1.25	W
		T <sub>A</sub> =70°C	0.8	W
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to 150	°C	

### THERMAL CHARACTERISTICS

R <sub>θ JA</sub>	Thermal Resistance, Junction-to-Ambient <sup>a</sup>	100	°C/W
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## ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>OFF CHARACTERISTICS</b>						
BV <sub>bss</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V			-1	uA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V			±10	uA
<b>ON CHARACTERISTICS</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-0.5	-1.1	-1.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.8A		43	52	m ohm
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.6A		52	65	m ohm
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1.8A		8.6		S
<b>DYNAMIC CHARACTERISTICS<sup>b</sup></b>						
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V f=1.0MHz		655		pF
C <sub>OSS</sub>	Output Capacitance			119		pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			94		pF
<b>SWITCHING CHARACTERISTICS<sup>b</sup></b>						
t <sub>D(ON)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-15V I <sub>D</sub> =-1A V <sub>GS</sub> =-10V R <sub>GEN</sub> = 6 ohm		9.2		ns
t <sub>r</sub>	Rise Time			13.5		ns
t <sub>D(OFF)</sub>	Turn-Off Delay Time			73		ns
t <sub>f</sub>	Fall Time			27		ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-15V, I <sub>D</sub> =-1.8A, V <sub>GS</sub> =-10V		13.7		nC
		V <sub>DS</sub> =-15V, I <sub>D</sub> =-1.8A, V <sub>GS</sub> =-4.5V		6.1		nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-15V, I <sub>D</sub> =-1.8A,		0.9		nC
Q <sub>gd</sub>	Gate-Drain Charge	V <sub>GS</sub> =-10V		4.3		nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A		-0.79	-1.2	V

### Notes

- a. Surface Mounted on FR4 Board of 1 inch<sup>2</sup>, 1oz.
- b. Guaranteed by design, not subject to production testing.
- c. Drain current limited by maximum junction temperature.

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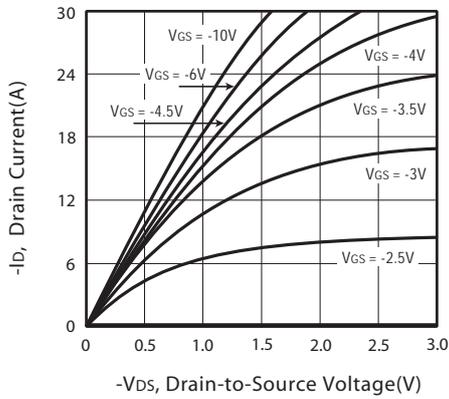


Figure 1. Output Characteristics

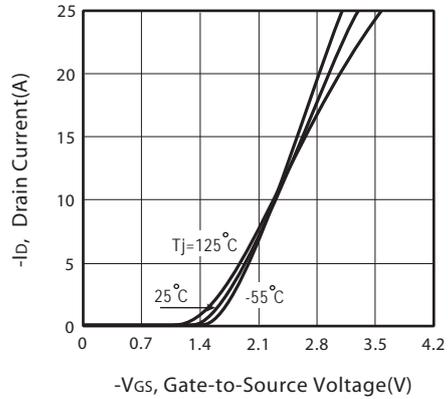


Figure 2. Transfer Characteristics

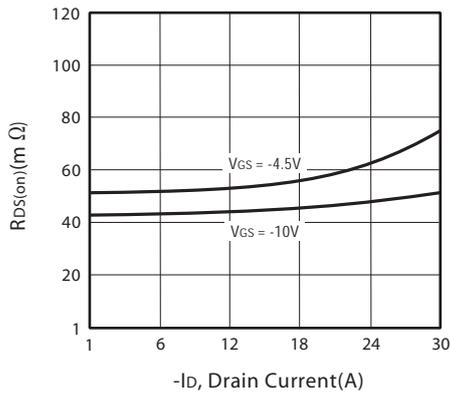


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

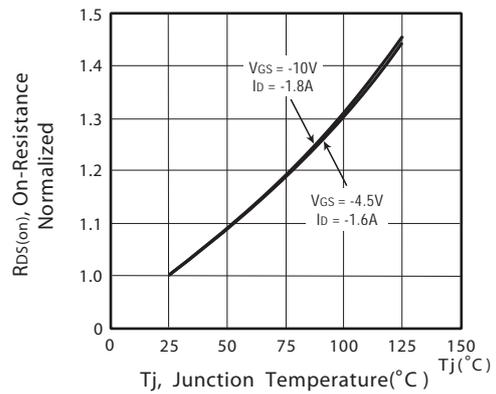


Figure 4. On-Resistance Variation with Drain Current and Temperature

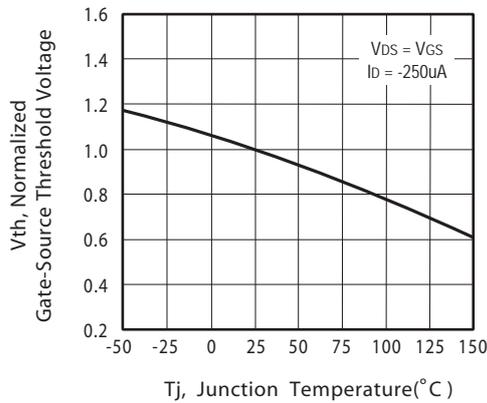


Figure 5. Gate Threshold Variation with Temperature

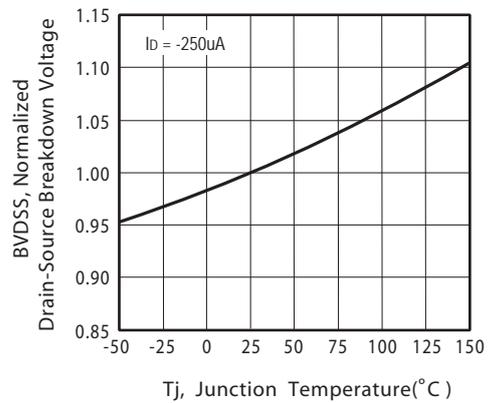


Figure 6. Breakdown Voltage Variation with Temperature

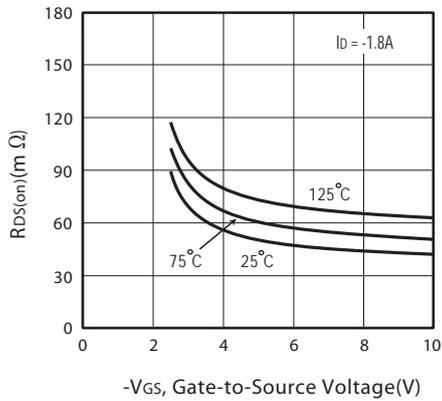


Figure 7. On-Resistance vs. Gate-Source Voltage

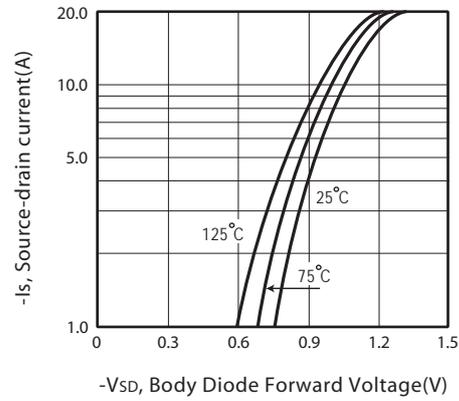


Figure 8. Body Diode Forward Voltage Variation with Source Current

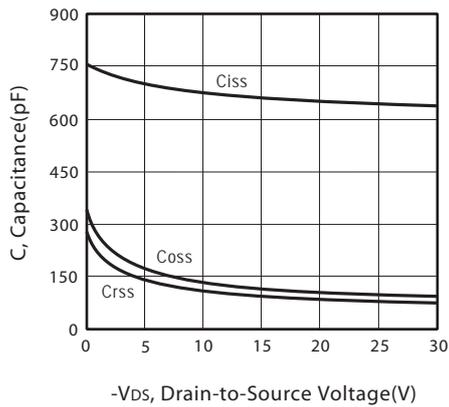


Figure 9. Capacitance

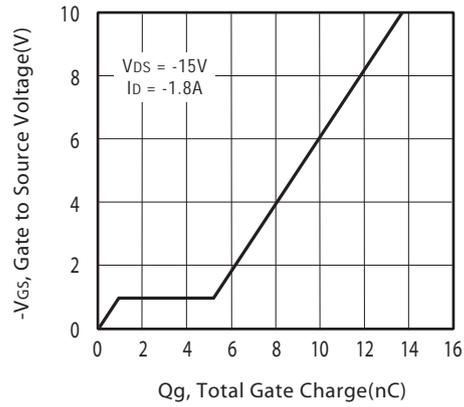


Figure 10. Gate Charge

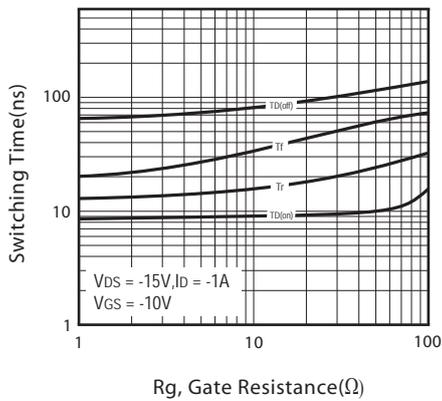


Figure 11. switching characteristics

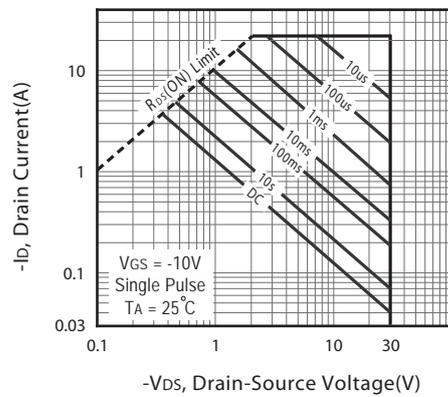


Figure 12. Maximum Safe Operating Area

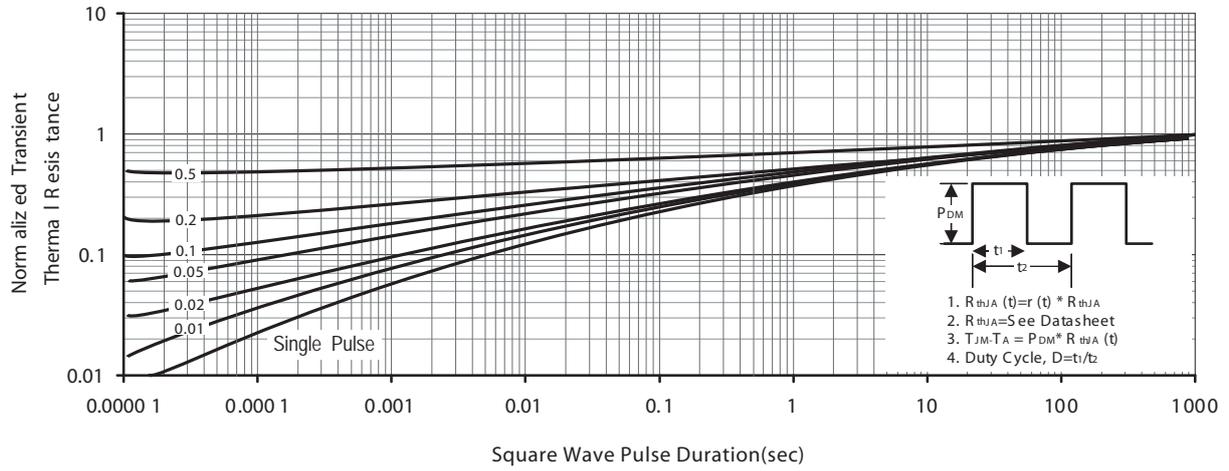


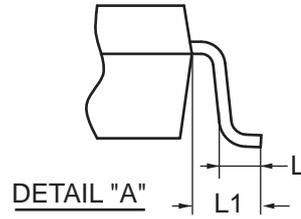
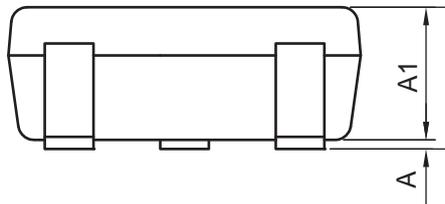
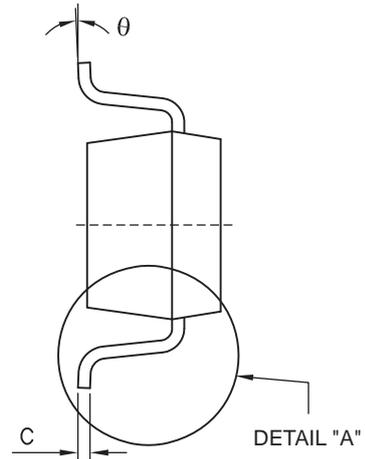
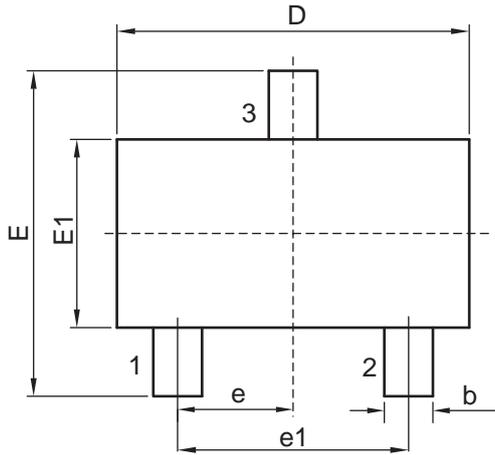
Figure 13. Normalized Thermal Transient Impedance Curve

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## PACKAGE OUTLINE DIMENSIONS

### SOT 23

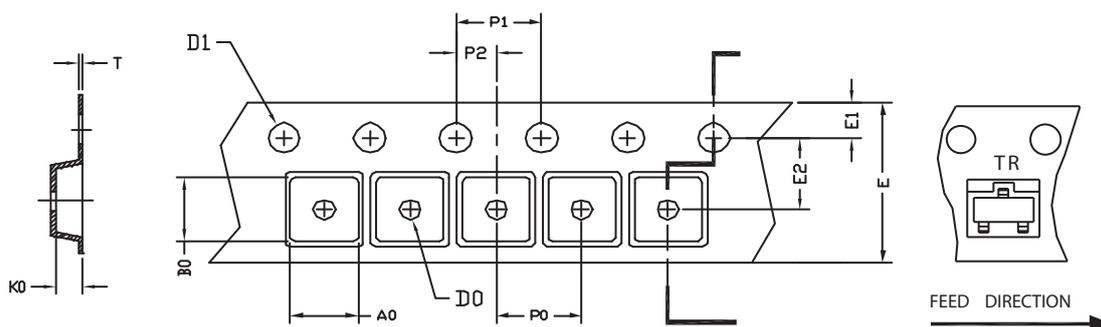


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
D	2.700	3.100	0.106	0.122
E	2.200	3.000	0.087	0.118
E1	1.200	1.700	0.047	0.067
e	0.850	1.150	0.033	0.045
e1	1.800	2.100	0.071	0.083
b	0.300	0.510	0.019	0.020
C	0.080	0.200	0.003	0.008
A	0.000	0.150	0.000	0.006
A1	0.887	1.300	0.035	0.051
L	0.450 REF.		0.018 REF.	
L1	0.600 REF.		0.024 REF.	
θ	0°	10°	0°	10°

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## SOT23 Tape and Reel Data

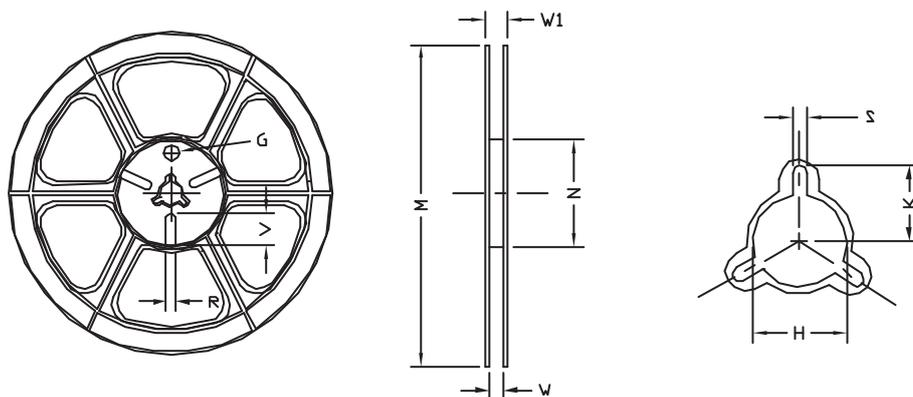
### SOT23-3L Carrier Tape



UNIT:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOT23-3L	3.15 $\pm 0.10$	2.77 $\pm 0.10$	1.22 $\pm 0.10$	$\phi$ 1.00 $+0.05$	$\phi$ 1.50 $+0.10$	8.00 $+0.30$ $-0.10$	1.75 $\pm 0.10$	3.50 $\pm 0.05$	4.00 $\pm 0.10$	4.00 $\pm 0.10$	2.00 $\pm 0.05$	0.22 $\pm 0.04$

### SOT23-3L Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
8mm	$\phi$ 178	$\phi$ 178 $\pm 1$	$\phi$ 60 $\pm 1$	9.00 $\pm 0.5$	12.00 $\pm 0.5$	$\phi$ 13.5 $\pm 0.5$	10.5	2.00 $\pm 0.5$	$\phi$ 10.0	5.00	18.00