



SamHop Microelectronics Corp.



# STM4800S

Dec.17 2013 Ver 1.1

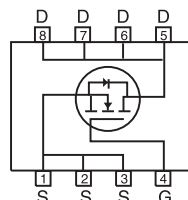
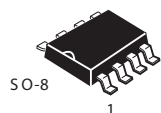
## N-Channel Enhancement Mode Field Effect Transistor

### PRODUCT SUMMARY

V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> (mΩ) Max
30V	8A	20 @ V <sub>GS</sub> = 10V
		28 @ V <sub>GS</sub> = 4.5V

### FEATURES

- Super high dense cell design for low R<sub>DS(ON)</sub>.
- Rugged and reliable.
- Surface Mount Package.



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous @ T <sub>j</sub> =25°C -Pulsed <sup>b</sup>	I <sub>D</sub>	8	A
	I <sub>DM</sub>	32	A
Drain-Source Diode Forward Current	I <sub>S</sub>	1.7	A
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	2.5	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	50	°C/W
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ELECTRICAL CHARACTERISTICS ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	$BV_{DS}$	$V_{GS}=0V, I_D=250\mu A$	30			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=24V, V_{GS}=0V$			1	$\mu A$
Gate-Body Leakage	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$			$\pm 100$	nA
ON CHARACTERISTICS <sup>b</sup>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.7	3	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=8A$		15	20	m ohm
		$V_{GS}=4.5V, I_D=7A$		19	28	m ohm
On-State Drain Current	$I_{D(ON)}$	$V_{DS}=10V, V_{GS}=10V$	10			A
Forward Transconductance	$g_{FS}$	$V_{DS}=10V, I_D=8A$		17		S
DYNAMIC CHARACTERISTICS <sup>c</sup>						
Input Capacitance	$C_{ISS}$	$V_{DS}=15V, V_{GS}=0V$ $f=1.0MHz$		820		pF
Output Capacitance	$C_{OSS}$			177		pF
Reverse Transfer Capacitance	$C_{RSS}$			60		pF
Gate resistance	$R_g$	$V_{GS}=0V, V_{DS}=0V, f=1.0MHz$		2.6		ohm
SWITCHING CHARACTERISTICS <sup>c</sup>						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD}=15V$ $I_D=1A$ $V_{GS}=10V$ $R_{GEN}=6\text{ ohm}$		10		ns
Rise Time	$t_r$			11		ns
Turn-Off Delay Time	$t_{D(OFF)}$			36		ns
Fall Time	$t_f$			9		ns
Total Gate Charge	$Q_g$	$V_{DS}=15V, I_D=8A, V_{GS}=10V$		15		nC
		$V_{DS}=15V, I_D=8A, V_{GS}=4.5V$		7.7		nC
Gate-Source Charge	$Q_{gs}$	$V_{DS}=15V, I_D=8A$ $V_{GS}=10V$		2.2		nC
Gate-Drain Charge	$Q_{gd}$			4.7		nC

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## ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS <sup>b</sup>						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 1.7A		0.8	1.2	V

- Notes
- a.Surface Mounted on FR4 Board, t <=10sec.
  - b.Pulse Test:Pulse Width ≤300us, Duty Cycle ≤ 2%.
  - c.Guaranteed by design, not subject to production testing.

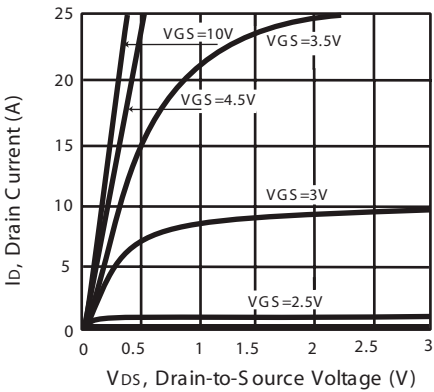


Figure 1. Output C haracteristics

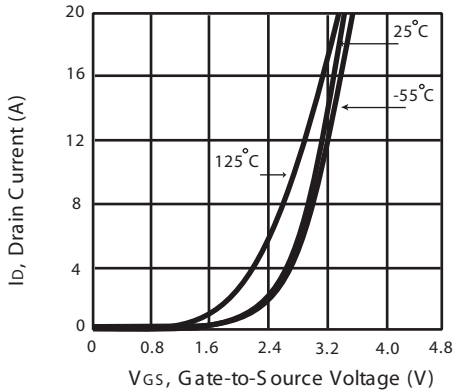


Figure 2. Transfer C haracteristics

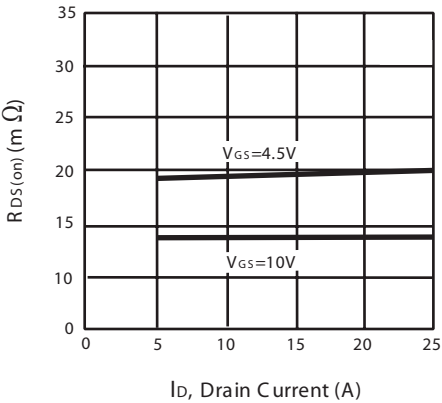


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

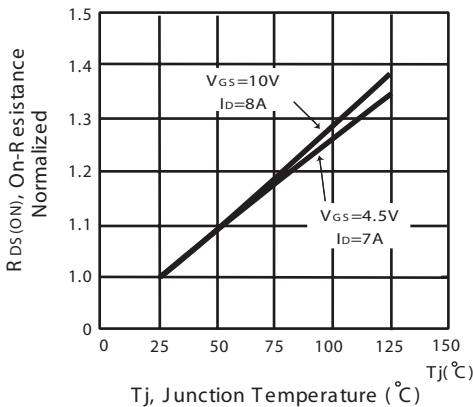


Figure 4. On-Resistance Variation with Drain Current and Temperature

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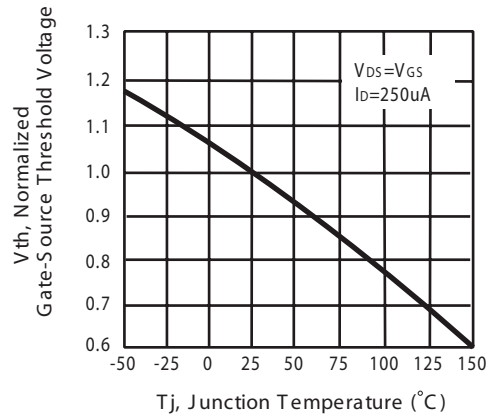


Figure 5. Gate Threshold Variation with Temperature

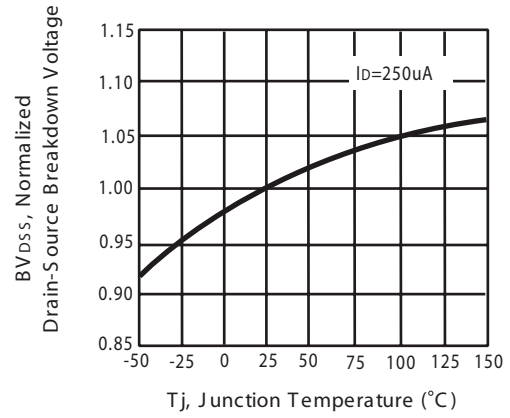


Figure 6. Breakdown Voltage Variation with Temperature

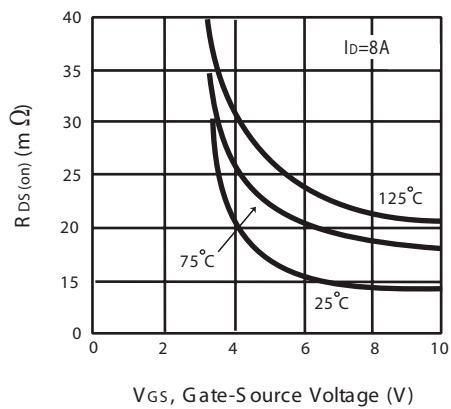


Figure 7. On-Resistance vs. Gate-Source Voltage

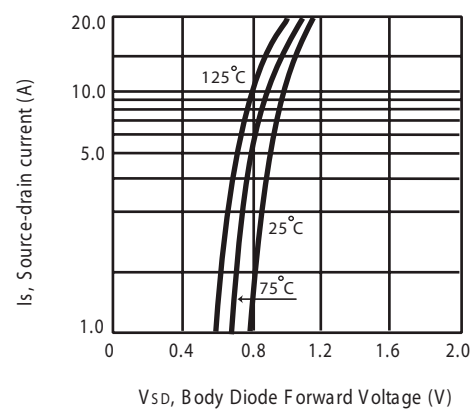


Figure 8. Body Diode Forward Voltage Variation with Source Current

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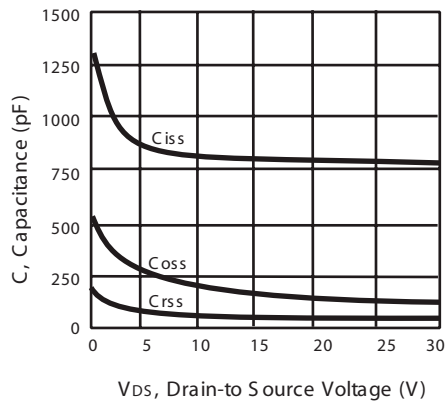


Figure 9. Capacitance

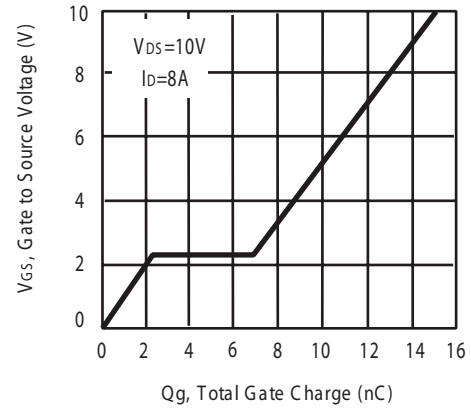


Figure 10. Gate Charge

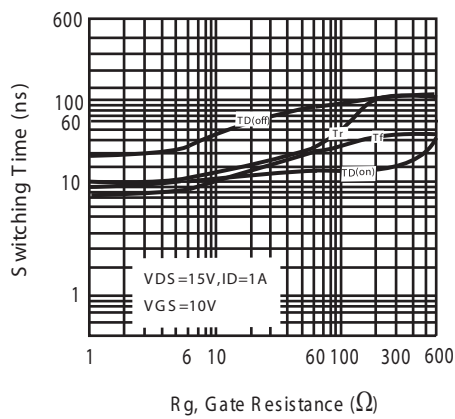


Figure 11. switching characteristics

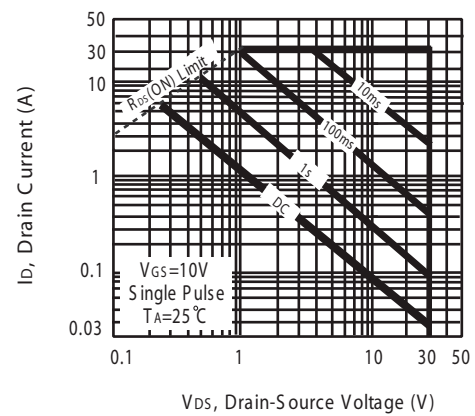


Figure 12. Maximum Safe Operating Area

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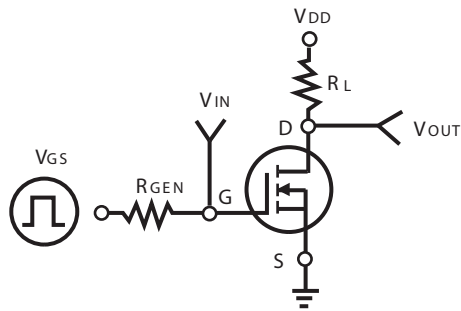


Figure 11. Switching Test Circuit

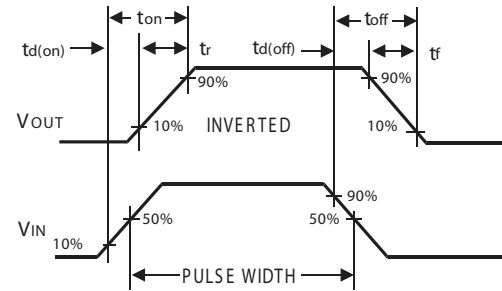


Figure 12. Switching Waveforms

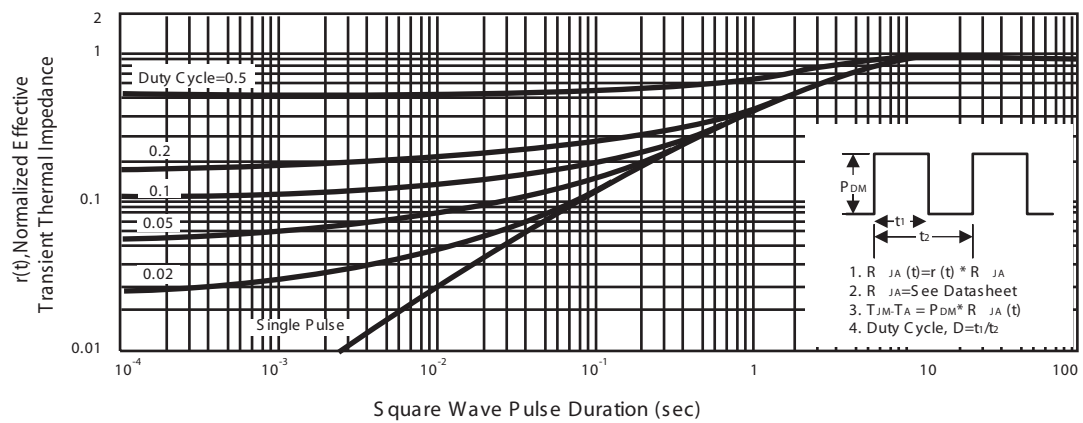
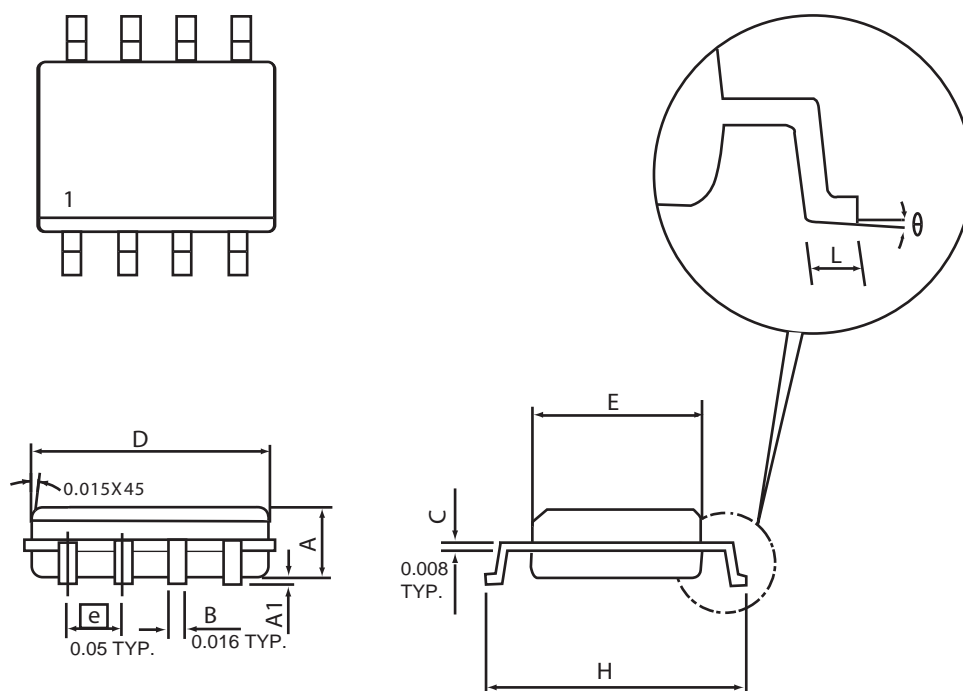


Figure 13. Normalized Thermal Transient Impedance Curve

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## Package Outline Dimensions

SO-8

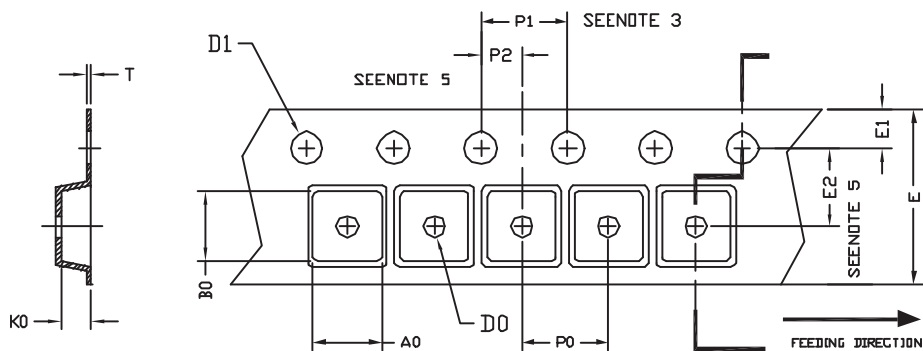


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0	8	0	8

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## SO-8 Tape and Reel Data

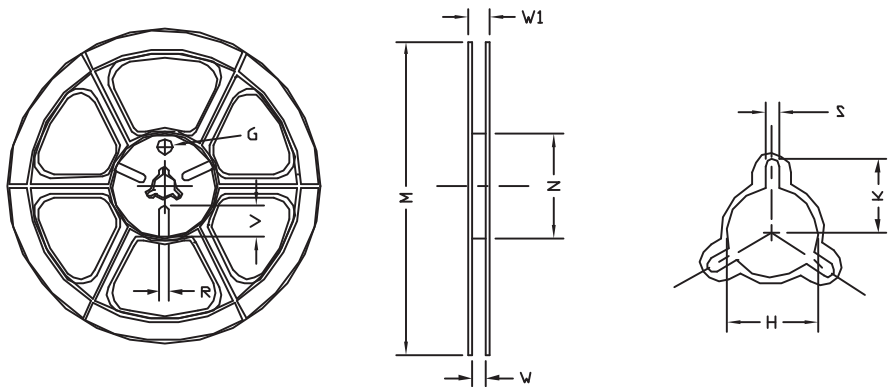
### SO-8 Carrier Tape



unit:

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150	6.40	5.20	2.10	1.5 (MIN)	1.5 + 0.1 - 0.0	12.0 0.3	1.75	5.5 0.05	8.0	4.0	2.0 0.05	0.3 0.05

### SO-8 Reel



UNIT:

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12	330	330 1	62 1.5	12.4 + 0.2	16.8 - 0.4	12.75 + 0.15	---	2.0 0.15	---	---	---



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## SO-8 Top Marking Definition

