



STP60L60F

SamHop Microelectronics Corp.

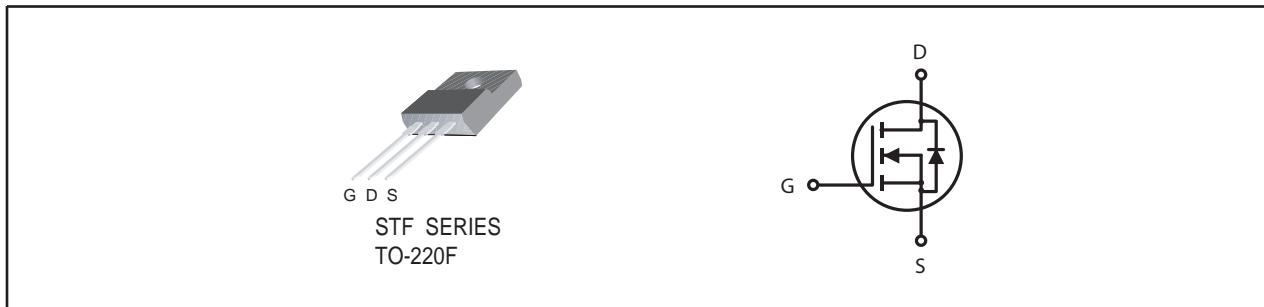
Ver 1.0

N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
VDSS	ID	RDS(ON) (mΩ) Typ
60V	32A	15 @ VGS=10V

FEATURES

- Super high dense cell design for low RDS(ON).
- Rugged and reliable.
- TO-220F Package.



ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous ^a	$T_C=25^\circ\text{C}$	A
		$T_C=70^\circ\text{C}$	A
I_{DM}	-Pulsed ^b	95	A
E_{AS}	Avalanche Energy ^d	144	mJ
P_D	Maximum Power Dissipation ^a	$T_C=25^\circ\text{C}$	W
		$T_C=70^\circ\text{C}$	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 to 175	°C

THERMAL CHARACTERISTICS

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	65	°C/W

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ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	60			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =48V , V _{GS} =0V			1	uA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V , V _{DS} =0V			±100	nA
ON CHARACTERISTICS						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	2.8	4	V
R _{D(S(ON))}	Drain-Source On-State Resistance	V _{GS} =10V , I _D =16A		15	19	m ohm
g _{FS}	Forward Transconductance	V _{DS} =20V , I _D =16A		25		S
DYNAMIC CHARACTERISTICS ^c						
C _{ISS}	Input Capacitance	V _{DS} =25V,V _{GS} =0V f=1.0MHz		2300		pF
C _{OSS}	Output Capacitance			142		pF
C _{RSS}	Reverse Transfer Capacitance			108		pF
SWITCHING CHARACTERISTICS ^c						
t _{D(ON)}	Turn-On Delay Time	V _{DD} =30V I _D =1A V _{GS} =10V R _{GEN} = 6 ohm		63		ns
t _r	Rise Time			71		ns
t _{D(OFF)}	Turn-Off Delay Time			162		ns
t _f	Fall Time			42		ns
Q _g	Total Gate Charge	V _{DS} =30V,I _D =25A,V _{GS} =10V		28		nC
Q _{gs}	Gate-Source Charge	V _{DS} =30V,I _D =25A, V _{GS} =10V		5		nC
Q _{gd}	Gate-Drain Charge			9.6		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
V _{SD}	Diode Forward Voltage	V _{GS} =0V,I _S =2A		0.78	1.3	V

Notes

- a.Surface Mounted on FR4 Board,t < 10sec.
- b.Pulse Test:Pulse Width < 300us, Duty Cycle < 2%.
- c.Guaranteed by design, not subject to production testing.
- d.Starting T_J=25°C,L=0.5mH,V_{DD} = 30V.(See Figure13)

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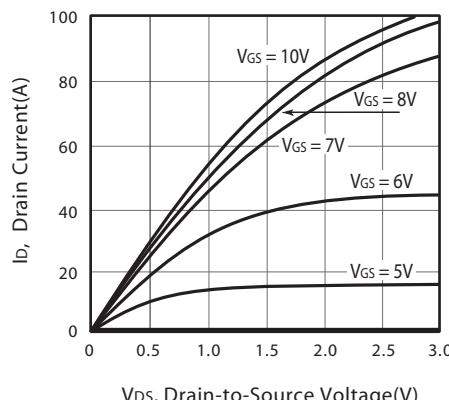


Figure 1. Output Characteristics

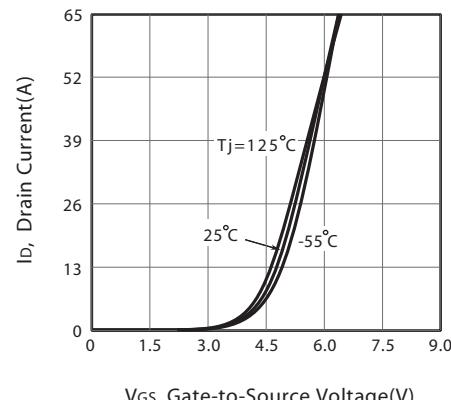


Figure 2. Transfer Characteristics

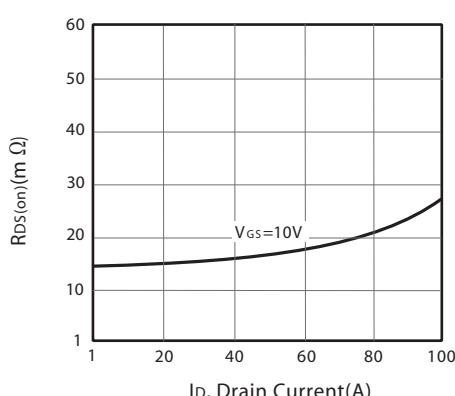


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

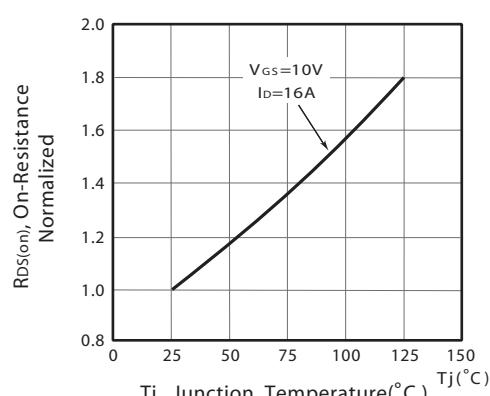


Figure 4. On-Resistance Variation with Drain Current and Temperature

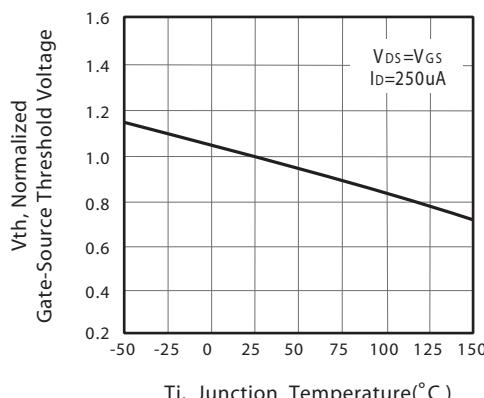


Figure 5. Gate Threshold Variation with Temperature

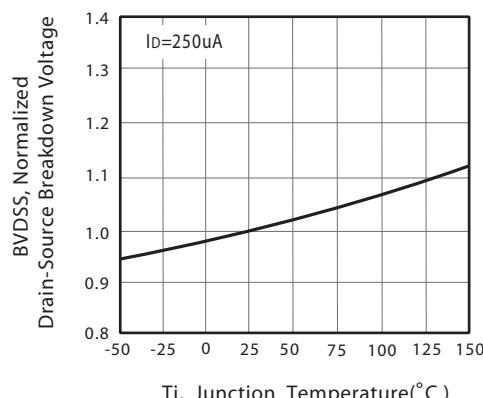
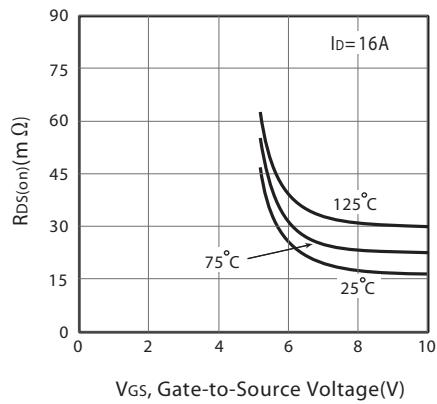


Figure 6. Breakdown Voltage Variation with Temperature

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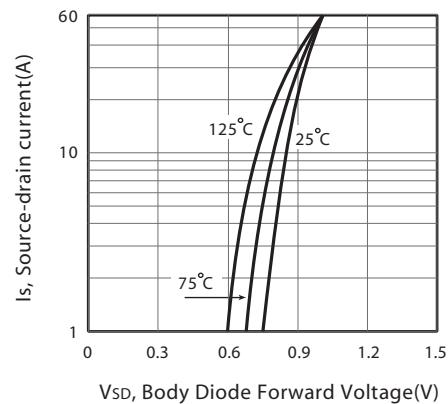
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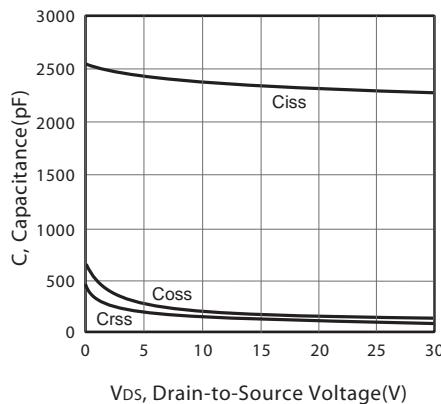
V_{GS}, Gate-to-Source Voltage(V)

Figure 7. On-Resistance vs. Gate-Source Voltage



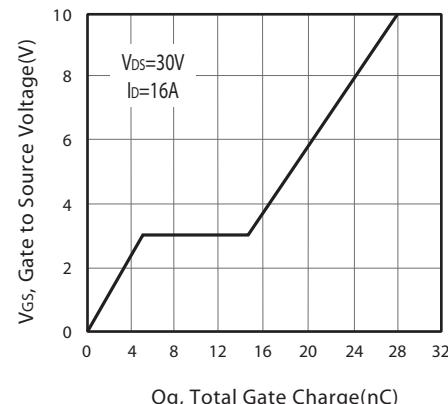
V_{SD}, Body Diode Forward Voltage(V)

Figure 8. Body Diode Forward Voltage Variation with Source Current



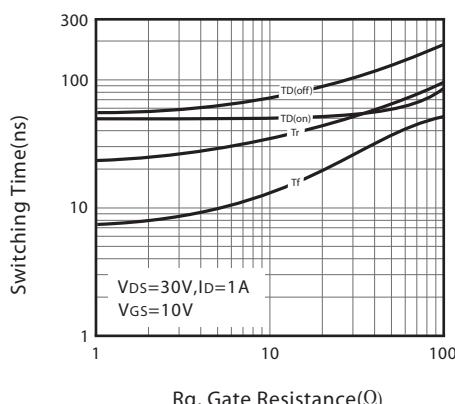
V_{DS}, Drain-to-Source Voltage(V)

Figure 9. Capacitance



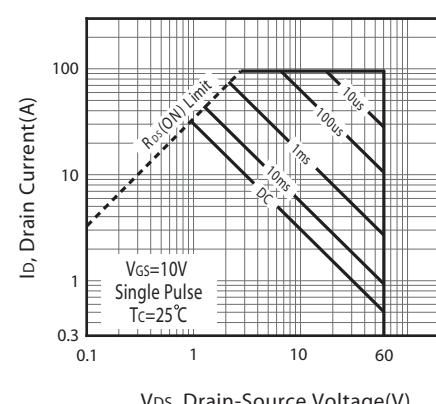
Q_g, Total Gate Charge(nC)

Figure 10. Gate Charge



R_g, Gate Resistance(Ω)

Figure 11. switching characteristics

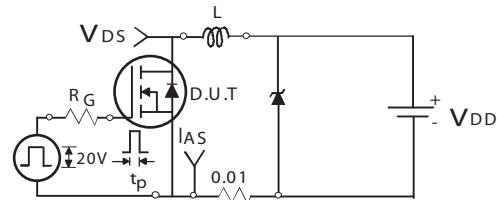


I_D, Drain Current(A)

Figure 12. Maximum Safe Operating Area

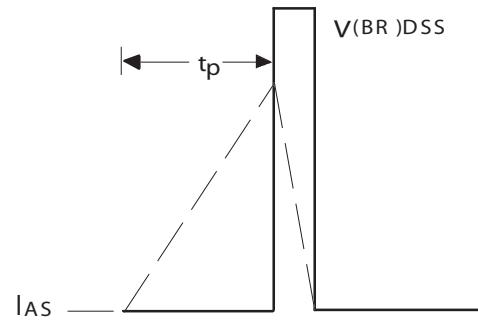
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Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

Figure 13b.

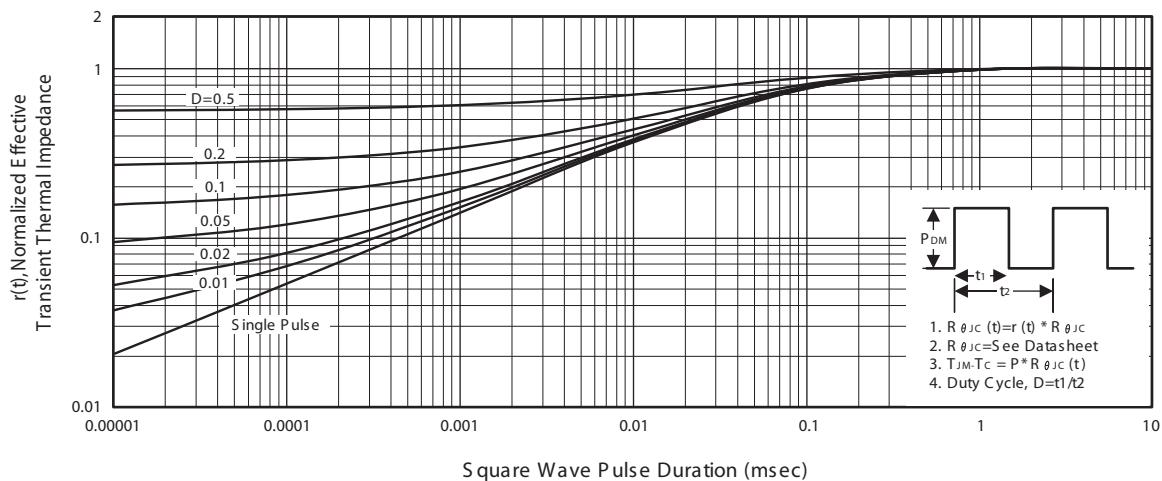


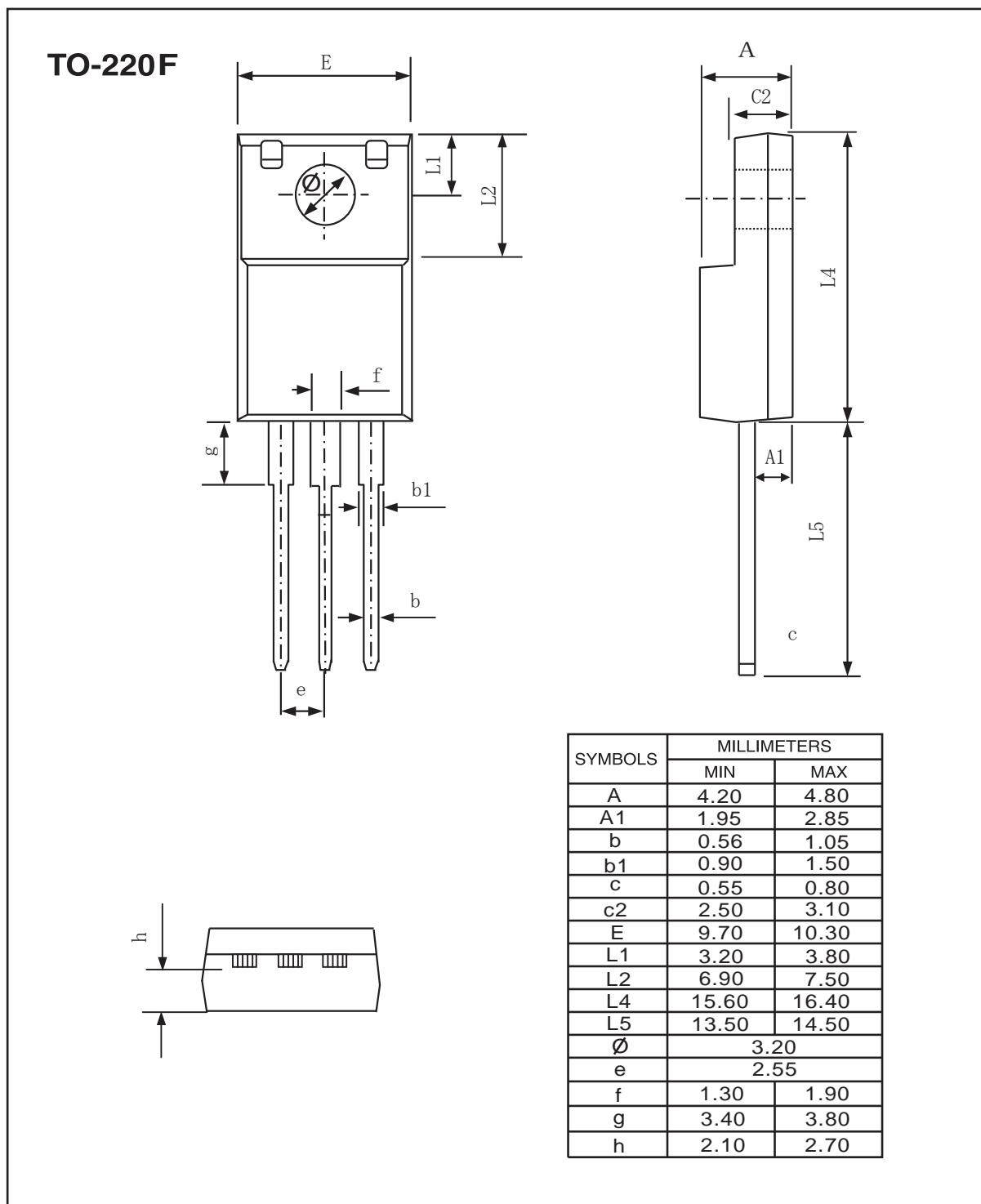
Figure 14. Normalized Thermal Transient Impedance Curve

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PACKAGE OUTLINE DIMENSIONS

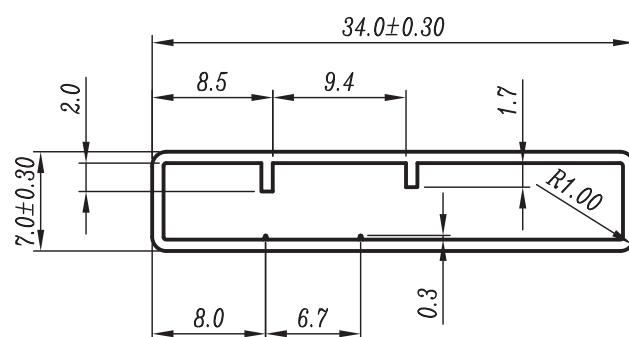
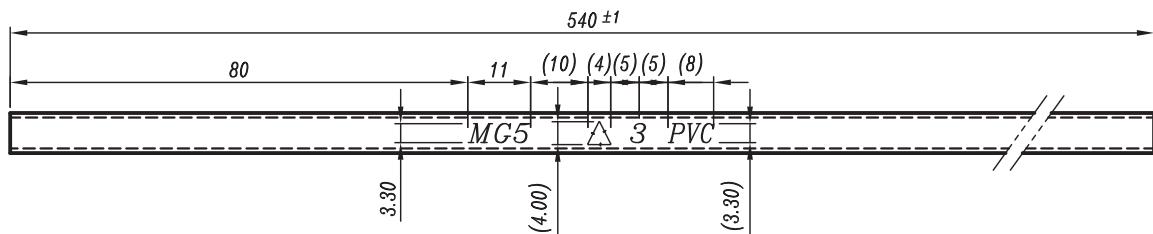


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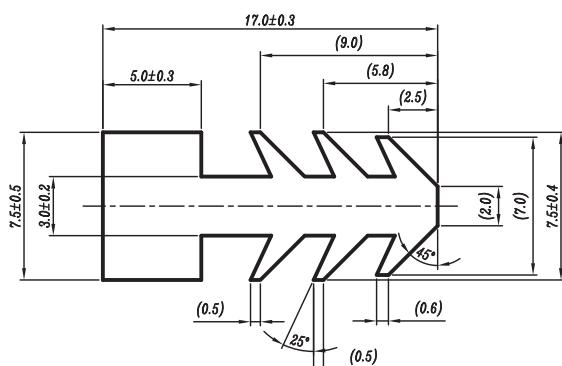
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TO-220F Tube



SCALE=2/1



$L = 8.0 \pm 0.5$

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