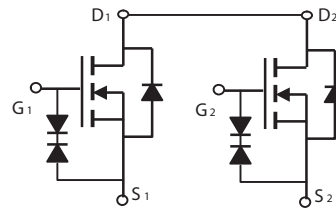
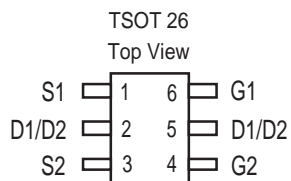


**Dual N-Channel Enhancement Mode Field Effect Transistor****PRODUCT SUMMARY**

V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> (mΩ) Max
20V	6A	20.0 @ V <sub>GS</sub> =4.5V
		21.0 @ V <sub>GS</sub> =4.0V
		21.5 @ V <sub>GS</sub> =3.7V
		24.5 @ V <sub>GS</sub> =3.1V
		29.5 @ V <sub>GS</sub> =2.5V

**FEATURES**

- Super high dense cell design for low R<sub>DS(ON)</sub>.
- Rugged and reliable.
- Surface Mount Package.
- ESD Protected.

**ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C unless otherwise noted)

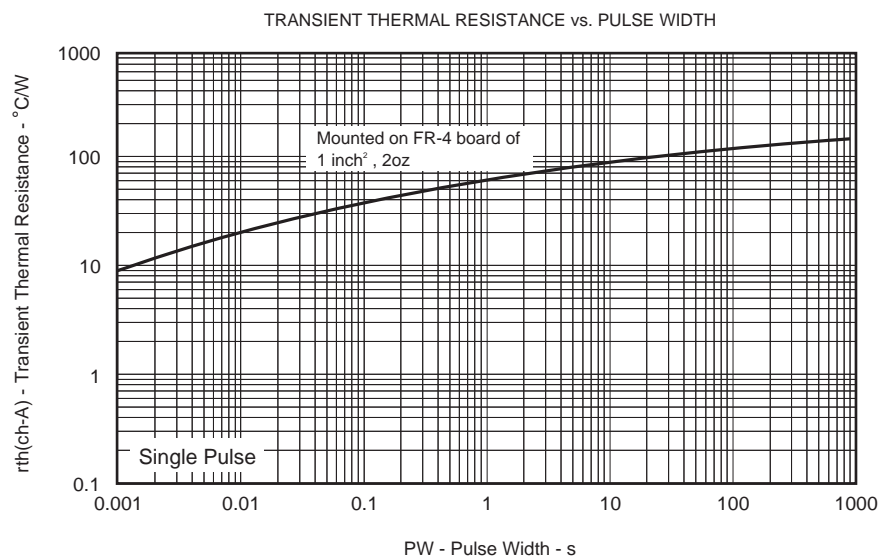
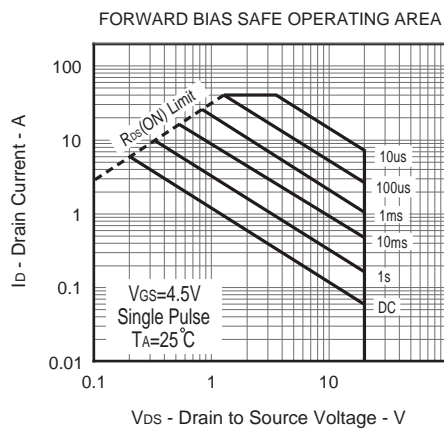
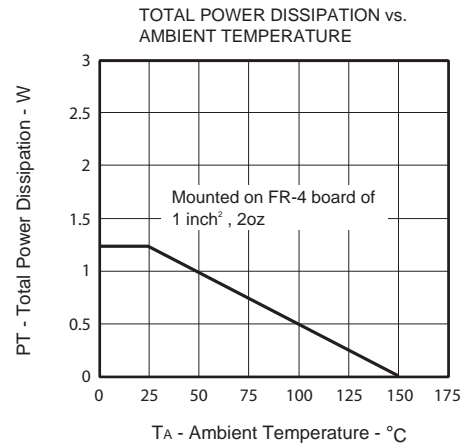
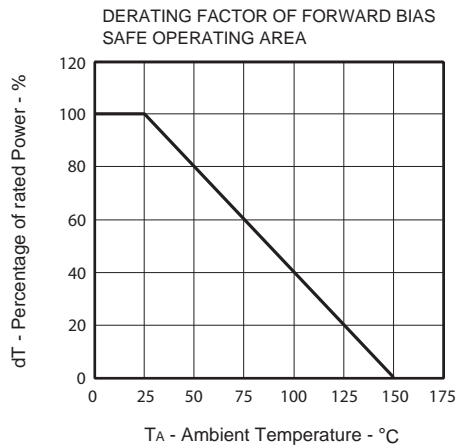
Symbol	Parameter	Limit	Units
V <sub>DS</sub>	Drain-Source Voltage	20	V
V <sub>GS</sub>	Gate-Source Voltage	±12	V
I <sub>D</sub>	Drain Current-Continuous <sup>a</sup>	T <sub>A</sub> =25°C	6.0
		T <sub>A</sub> =70°C	4.8
I <sub>DM</sub>	-Pulsed <sup>b</sup>	40	A
P <sub>D</sub>	Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	1.25
		T <sub>A</sub> =70°C	0.8
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to 150	°C

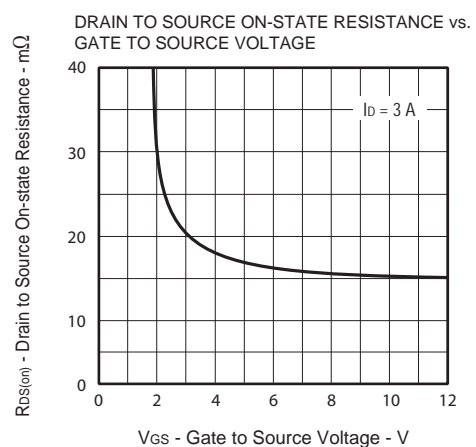
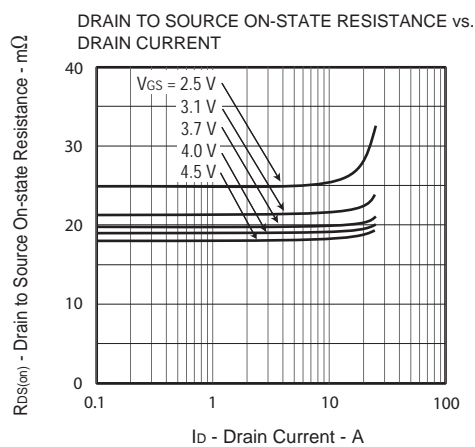
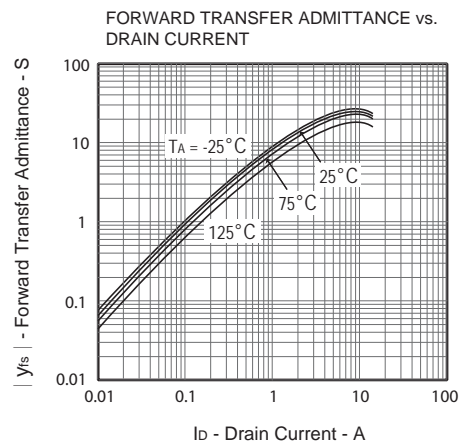
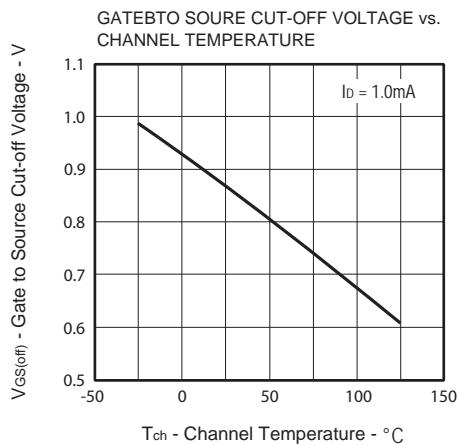
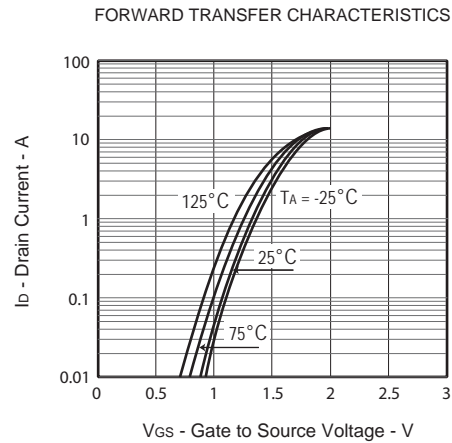
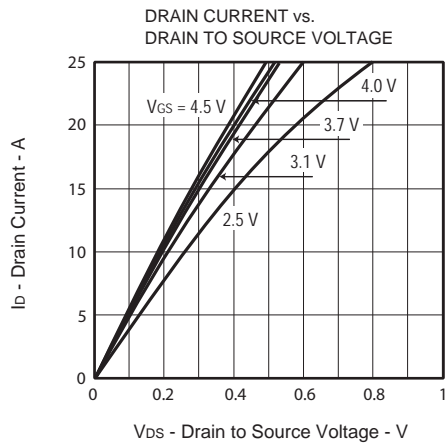
**THERMAL CHARACTERISTICS**

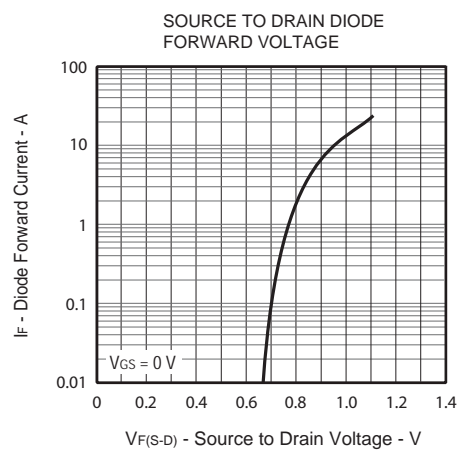
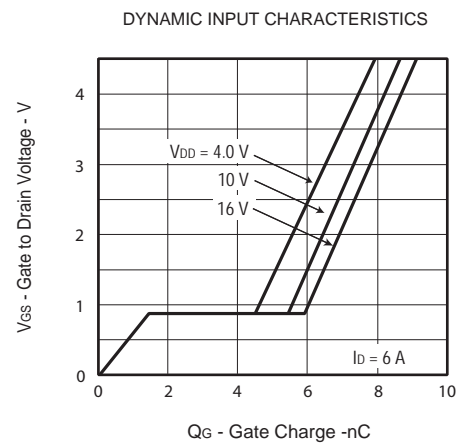
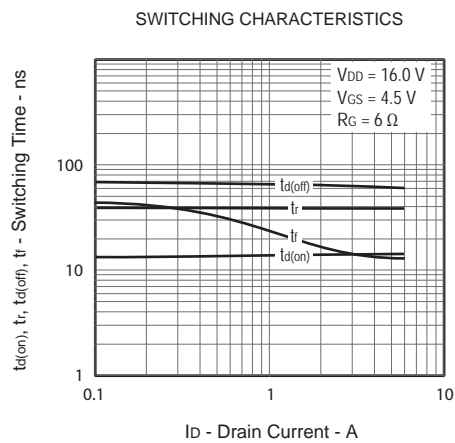
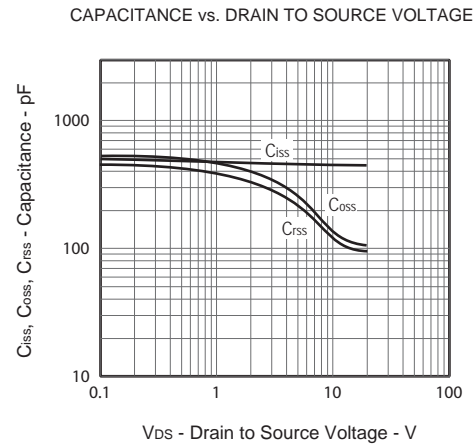
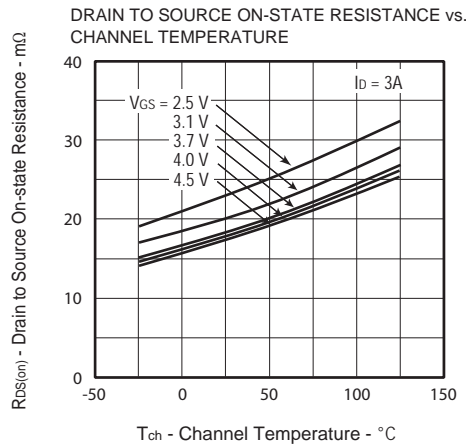
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	100	°C/W
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## ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	20			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =16V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> = ±12V , V <sub>DS</sub> =0V			±10	uA
ON CHARACTERISTICS						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =1mA	0.5	0.9	1.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =3A	15.5	18.0	20.0	m ohm
		V <sub>GS</sub> =4.0V , I <sub>D</sub> =3A	16.0	18.5	21.0	m ohm
		V <sub>GS</sub> =3.7V , I <sub>D</sub> =3A	16.5	19.5	21.5	m ohm
		V <sub>GS</sub> =3.1V , I <sub>D</sub> =3A	17.5	21.0	24.5	m ohm
		V <sub>GS</sub> =2.5V , I <sub>D</sub> =3A	19.5	24.5	29.5	m ohm
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =3A		17		S
DYNAMIC CHARACTERISTICS <sup>c</sup>						
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V f=1.0MHz		463		pF
C <sub>OSS</sub>	Output Capacitance			156		pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			142		pF
SWITCHING CHARACTERISTICS <sup>c</sup>						
t <sub>D(ON)</sub>	Turn-On Delay Time	V <sub>DD</sub> =16V I <sub>D</sub> =3A V <sub>GS</sub> =4.5V R <sub>GEN</sub> = 6 ohm		15		ns
t <sub>r</sub>	Rise Time			51		ns
t <sub>D(OFF)</sub>	Turn-Off Delay Time			43		ns
t <sub>f</sub>	Fall Time			30		ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =16V,I <sub>D</sub> =6A, V <sub>GS</sub> =4.5V		9.1		nC
Q <sub>gs</sub>	Gate-Source Charge			1.4		nC
Q <sub>gd</sub>	Gate-Drain Charge			4.5		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V,I <sub>S</sub> =0.7A		0.76	1.2	V
Notes						
a.Surface Mounted on FR4 Board,t < 10sec.						
b.Pulse Test:Pulse Width < 10us, Duty Cycle < 1%.						
c.Guaranteed by design, not subject to production testing.						

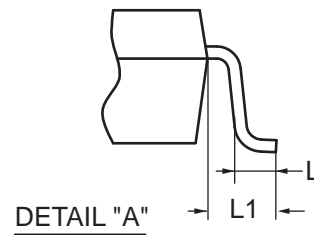
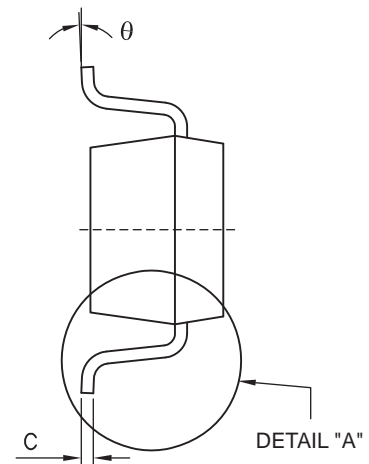
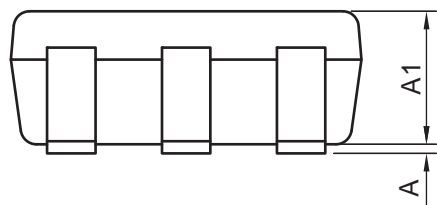
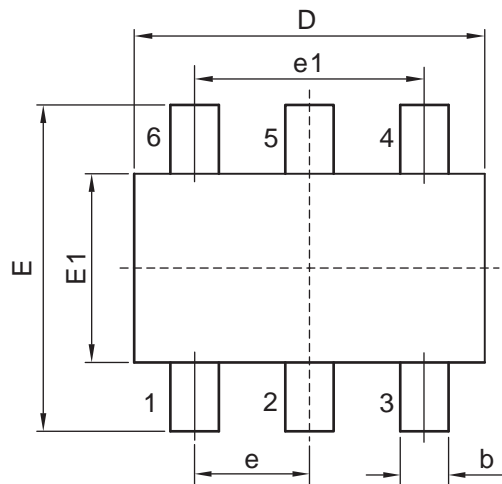






## PACKAGE OUTLINE DIMENSIONS

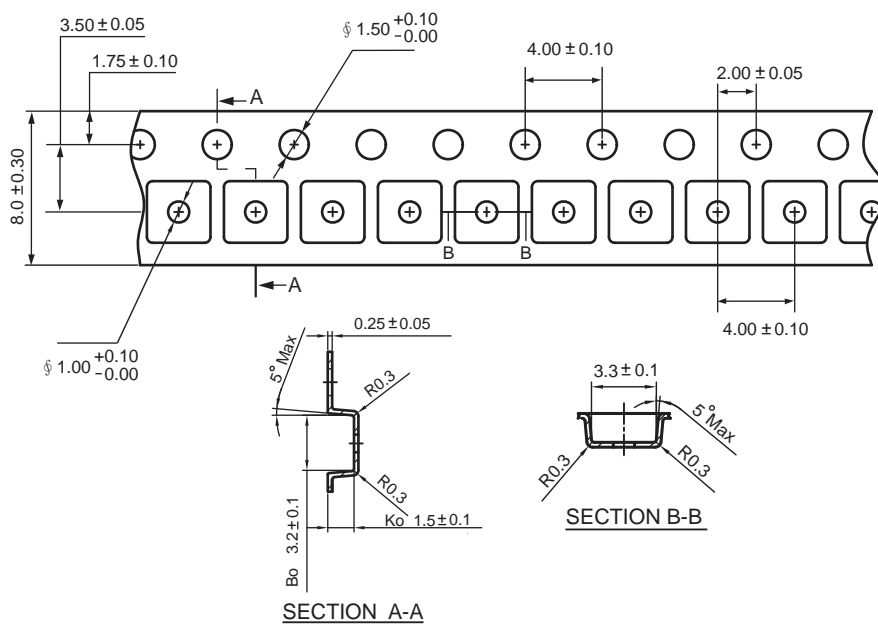
### TSOT 26



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
D	2.692	3.099	0.106	0.122
E	2.591	3.000	0.102	0.118
E1	1.397	1.803	0.055	0.071
e	0.950 REF.		0.037 REF.	
e1	1.900 REF.		0.075 REF.	
b	0.300	0.500	0.012	0.020
C	0.080	0.200	0.003	0.008
A	0.000	0.100	0.000	0.004
A1	0.700	0.800	0.028	0.032
L	0.300	0.600	0.012	0.024
L1	0.600 REF.		0.023 REF.	
$\theta$	0°	6°	0°	6°

## TSOT 26 Tape and Reel Data

### TSOT 26 Carrier Tape



### TSOT 26 Reel

