



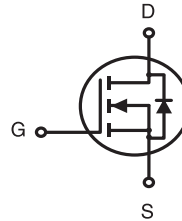
## N-Channel Logic Level Enhancement Mode Field Effect Transistor

### PRODUCT SUMMARY

VDSS	ID	RDS(ON) (mΩ) Max
150V	32A	46 @ VGS=10V
		50 @ VGS=4.5V

### FEATURES

- Super high dense cell design for low RDS(ON).
- Rugged and reliable.
- TO-263 package.



### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub>=25°C unless otherwise noted)

Symbol	Parameter	Limit	Units
V <sub>DS</sub>	Drain-Source Voltage	150	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current-Continuous <sup>c</sup>	T <sub>C</sub> =25°C	32
		T <sub>C</sub> =100°C	22.6
I <sub>DM</sub>	-Pulsed <sup>a,c</sup>	94	A
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>d</sup>	216	mJ
P <sub>D</sub>	Maximum Power Dissipation	T <sub>C</sub> =25°C	136
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to 175	°C

### THERMAL CHARACTERISTICS

R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	1.1	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	55	°C/W

# STB28N15

Ver 1.0

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>OFF CHARACTERISTICS</b>						
B <sub>V</sub> DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	150			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =120V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> = ±20V , V <sub>DS</sub> =0V			±100	nA
<b>ON CHARACTERISTICS</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.2		2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		35	46	m ohm
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A		37	50	m ohm
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A		55		S
<b>DYNAMIC CHARACTERISTICS<sup>b</sup></b>						
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V f=1.0MHz		3760		pF
C <sub>OSS</sub>	Output Capacitance			210		pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			163		pF
<b>SWITCHING CHARACTERISTICS<sup>b</sup></b>						
t <sub>D(ON)</sub>	Turn-On Delay Time	V <sub>DD</sub> =50V I <sub>D</sub> =10A V <sub>GS</sub> =4.5V R <sub>GEN</sub> = 3.3 ohm		20		ns
t <sub>r</sub>	Rise Time			22		ns
t <sub>D(OFF)</sub>	Turn-Off Delay Time			68		ns
t <sub>f</sub>	Fall Time			16		ns
Q <sub>g</sub>	Total Gate Charge		V <sub>DS</sub> =75V, I <sub>D</sub> =10A, V <sub>GS</sub> =4.5V		41	
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =75V, I <sub>D</sub> =10A, V <sub>GS</sub> =4.5V		11		nC
Q <sub>gd</sub>	Gate-Drain Charge			22		nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A			1.2	V

### Notes

- a. Pulse Test: Pulse Width ≤ 10us, Duty Cycle ≤ 1%.
- b. Guaranteed by design, not subject to production testing.
- c. Drain current limited by maximum junction temperature.
- d. Starting T<sub>J</sub>=25°C, L=0.3mH, V<sub>DD</sub>= 25V.
- e. Mounted on FR4 Board of 1 inch<sup>2</sup> , 2oz.

Apr,08,2016

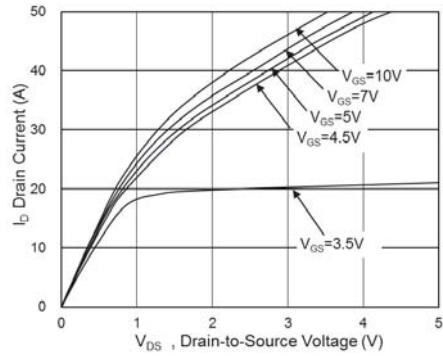


Fig.1 Typical Output Characteristics

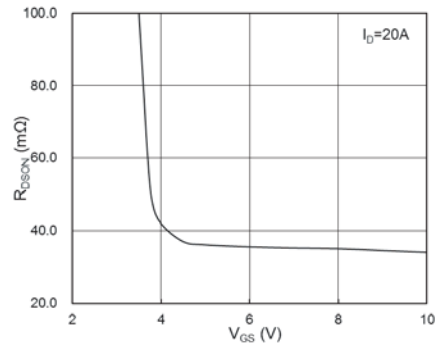


Fig.2 On-Resistance vs. Gate-Source

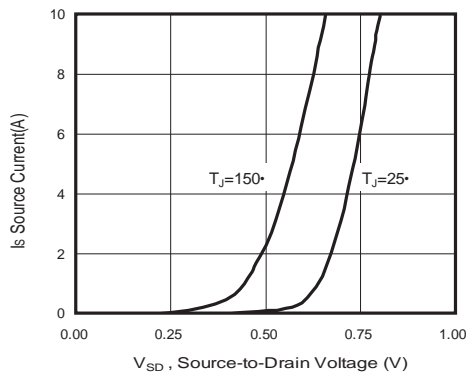


Fig.3 Forward Characteristics Of Reverse

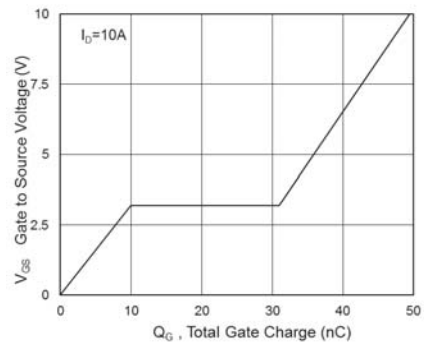


Fig.4 Gate-Charge Characteristics

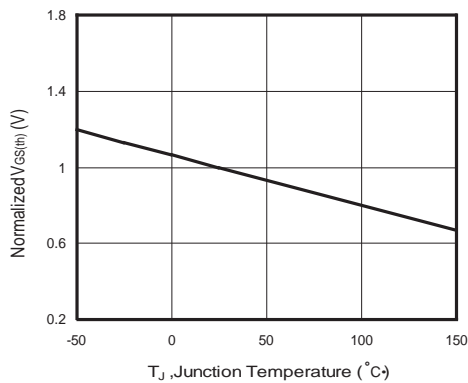


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

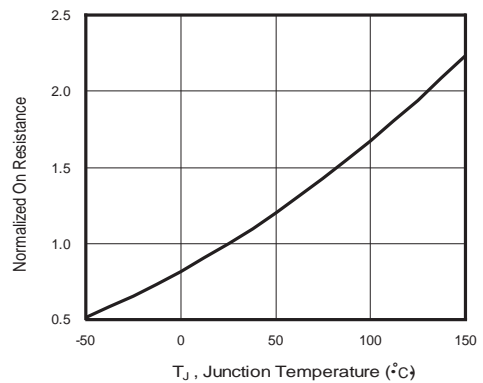
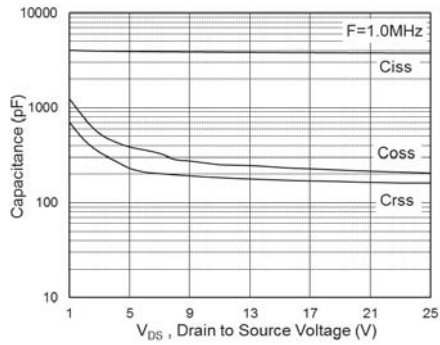
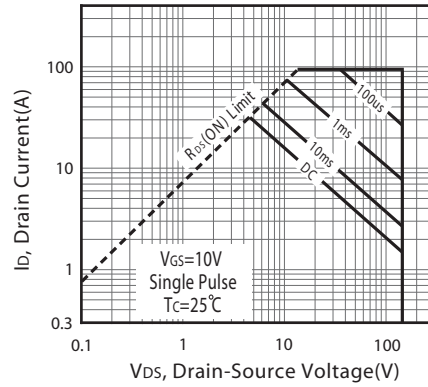


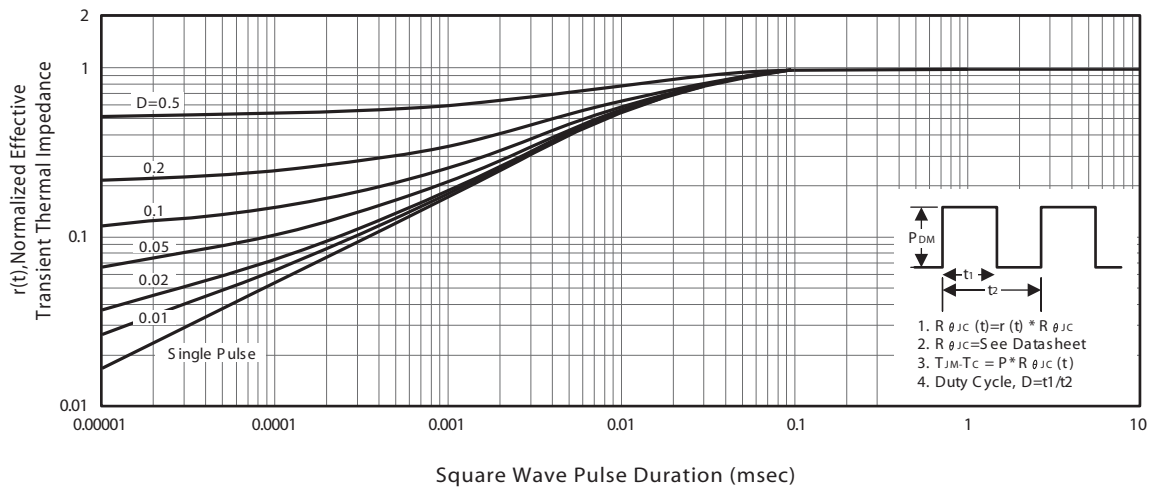
Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$



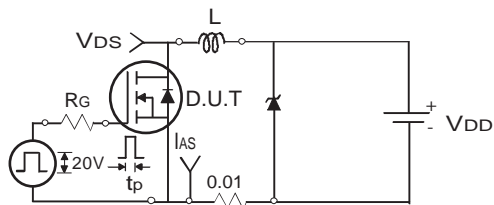
**Fig.7 Capacitance**



**Fig.8 Safe Operating Area**

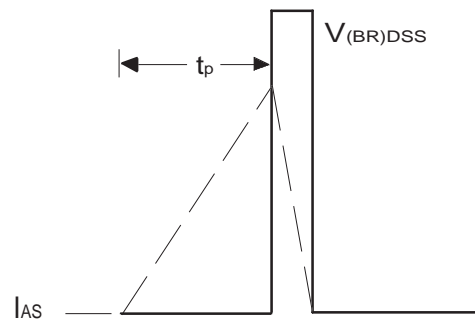


**Figure 9. Normalized Thermal Transient Impedance Curve**



**Uncamped Inductive Test Circuit**

**Fig.10a**

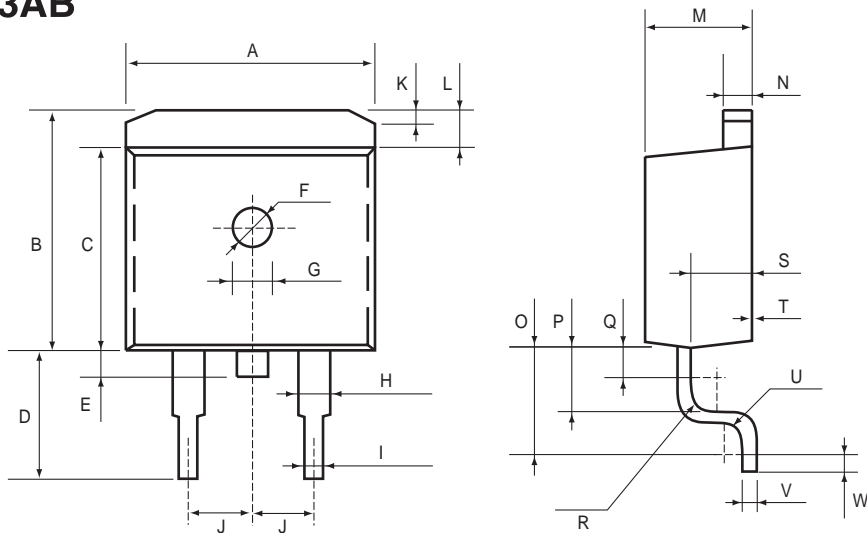


**Unclamped Inductive Waveforms**

**Fig.10b**

## PACKAGE OUTLINE DIMENSIONS

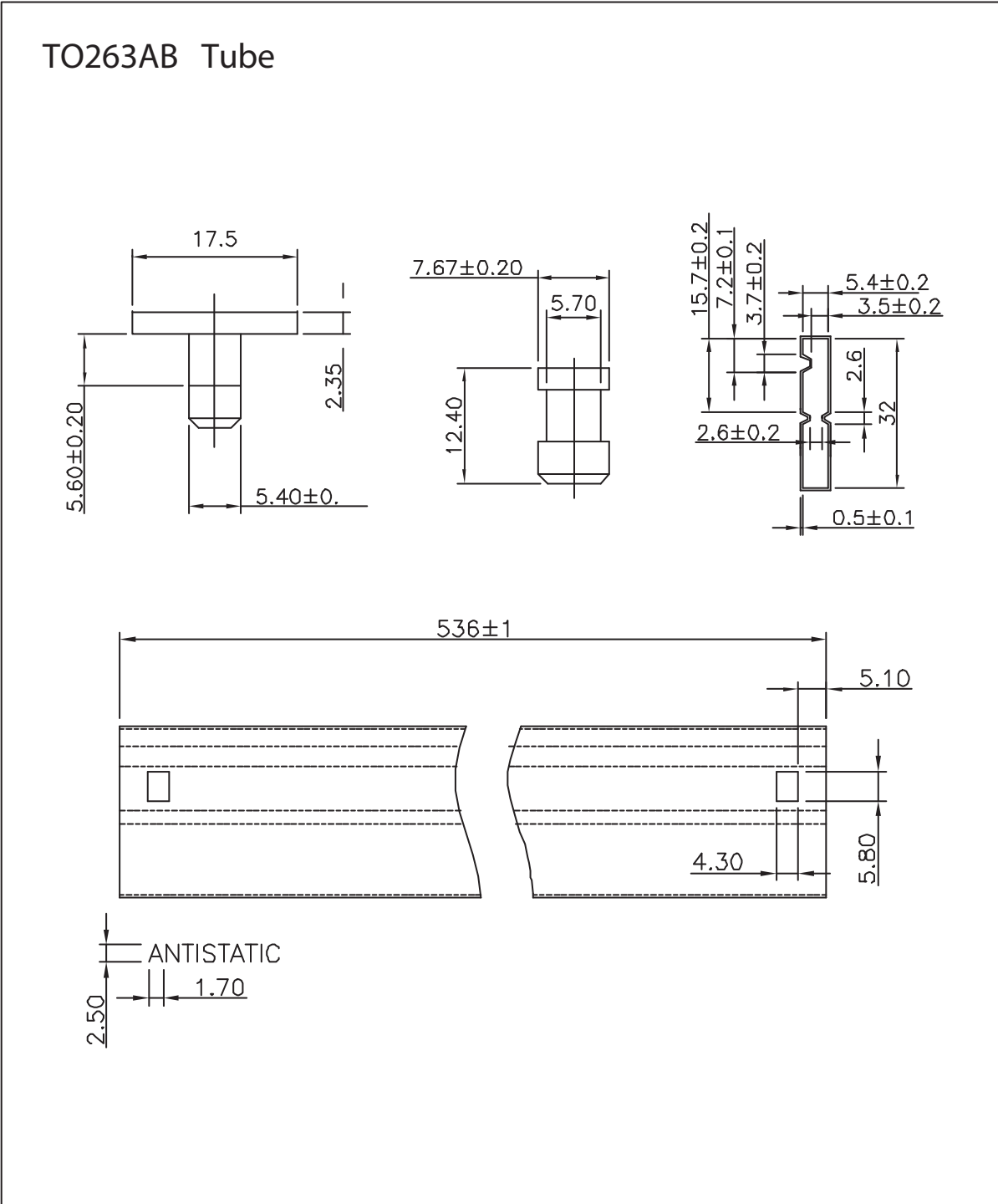
### TO-263AB



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.9	10.5	0.390	0.413
B	9.5	10.3	0.374	0.406
C	8.3	8.9	0.327	0.350
D	4.7	5.5	0.185	0.217
E	1.5		0.059	
F	$\phi$ 1.6		$\phi$ 0.063	
G	1.0	1.4	0.039	0.055
H	1.07	1.47	0.042	0.058
I	0.76	1.06	0.030	0.042
J	2.04	3.04	0.080	0.120
K	0.2	0.6	0.0079	0.024
L	1.4		0.055	
M	4.24	4.64	0.167	0.183
N	1.15	1.45	0.045	0.057
O	3.25	3.75	0.128	0.148
P	2.3		0.091	
Q	1.6		0.063	
R	R0.4	R1.0	R0.0158	R0.0394
S	2.7 MAX		0.106 MAX	
T	0.0	0.3	0.0000	0.0118
U	R0.4	R1.0	R0.0158	R0.0394
V	0.3	0.5	0.0118	0.0197
W	1.2 min		0.047 min	

# STB28N15

Ver 1.0

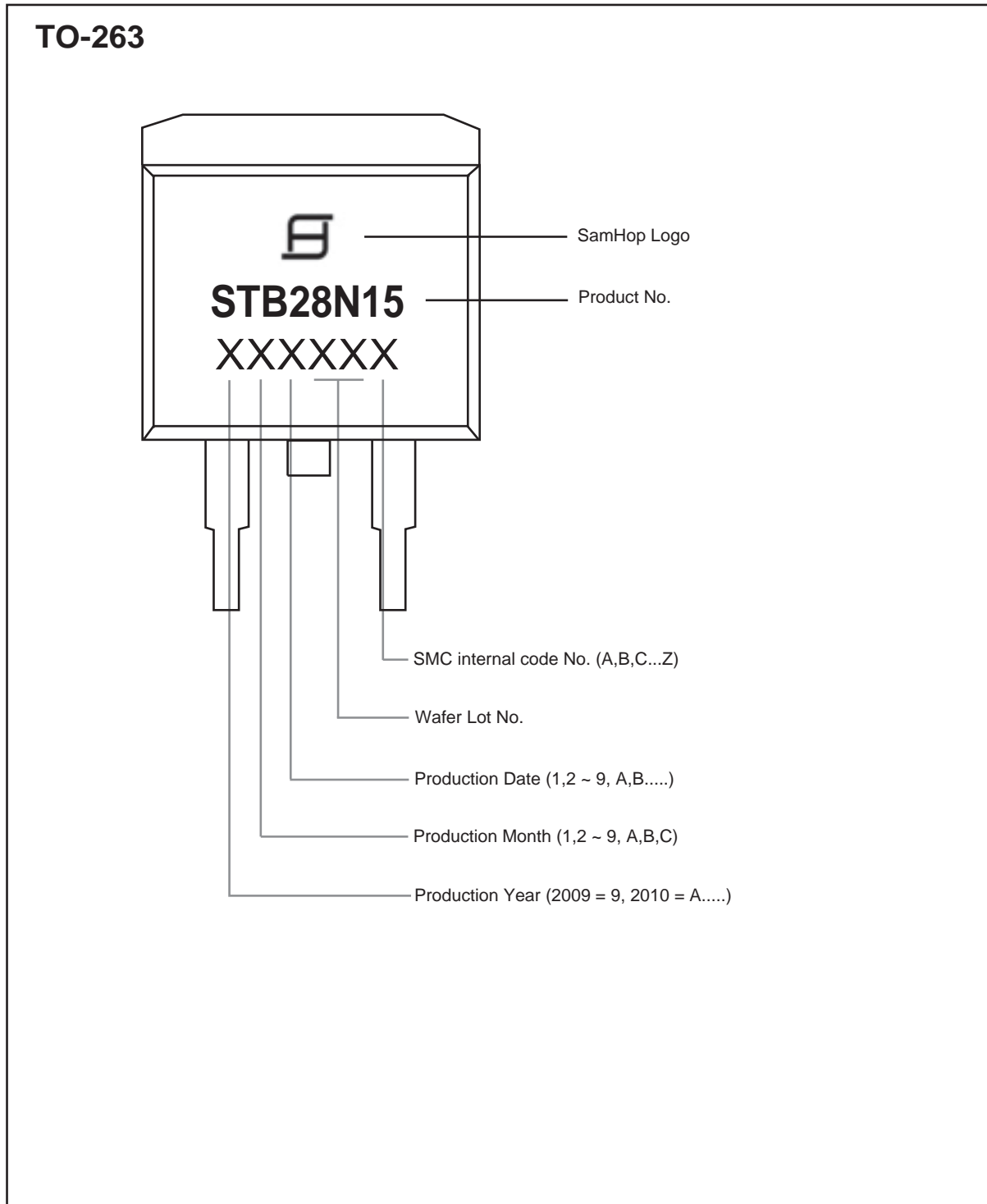


Apr,08,2016

# STB28N15

Ver 1.0

## TOP MARKING DEFINITION



Apr,08,2016